1. Issue 7 Circuit Diagrams

1.1 List of Illustrations

Issue 7 Circuit Diagrams

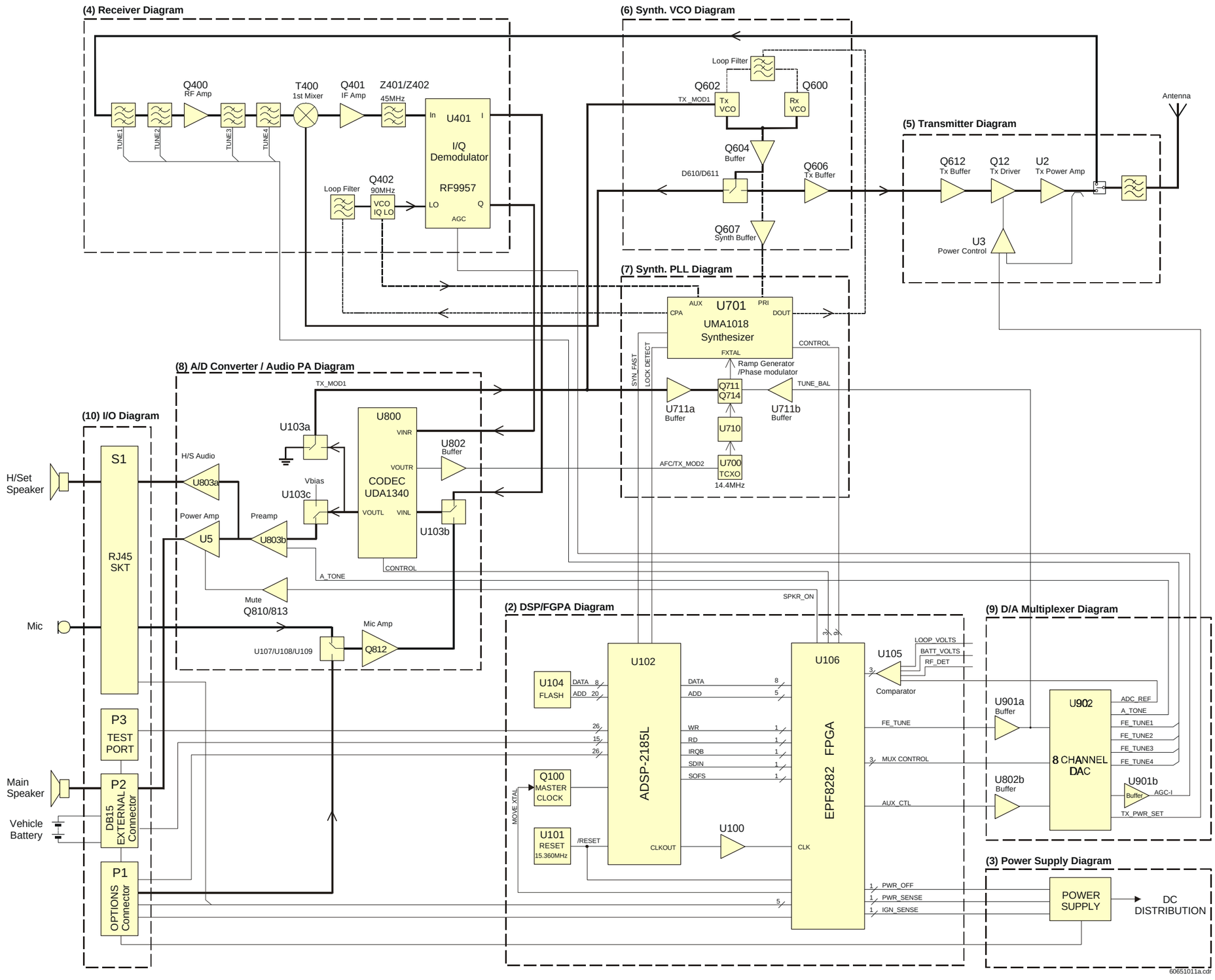


Figure 1 SRM9000 Block Diagram

Issue 7 Circuit Diagrams

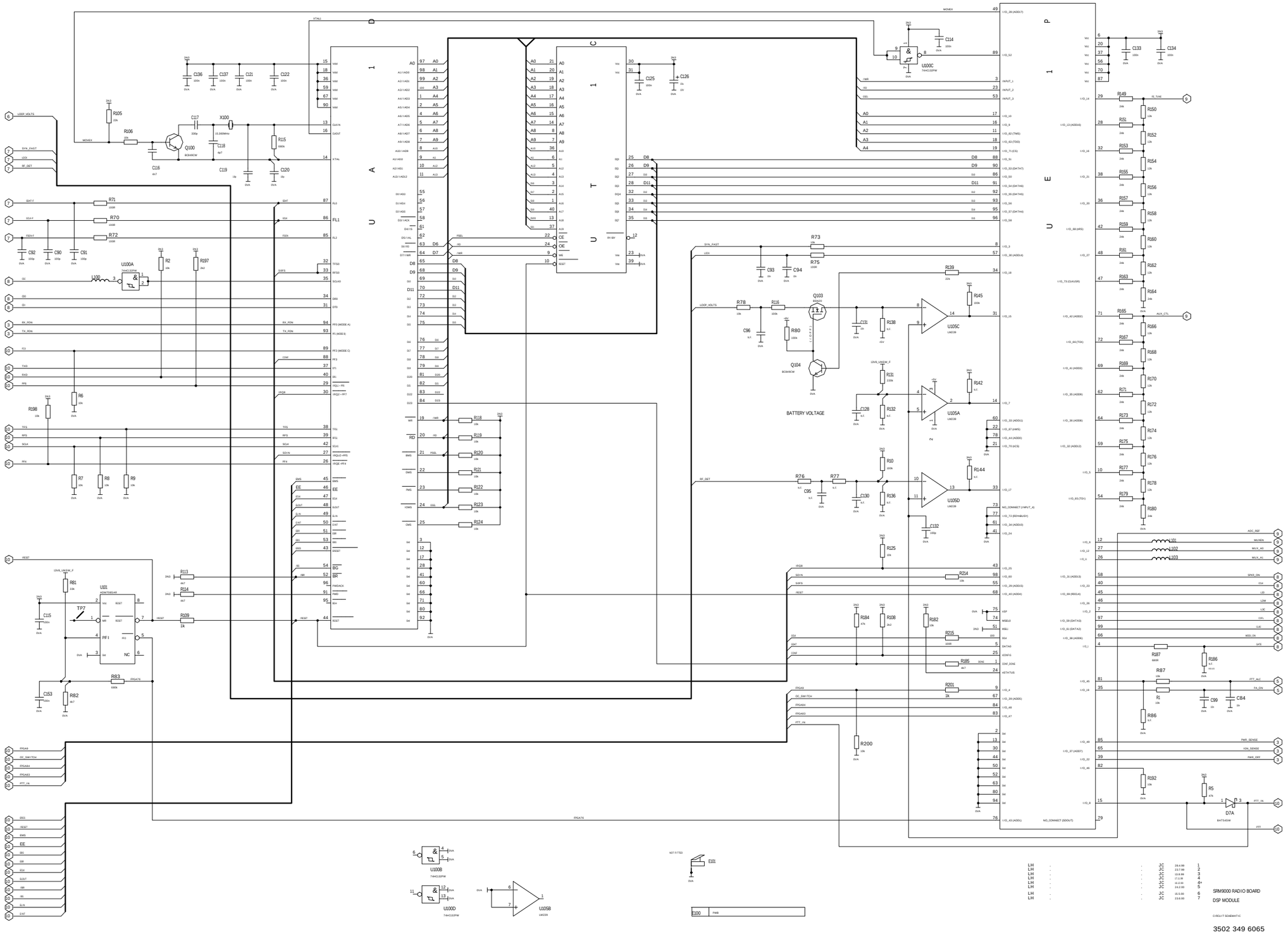
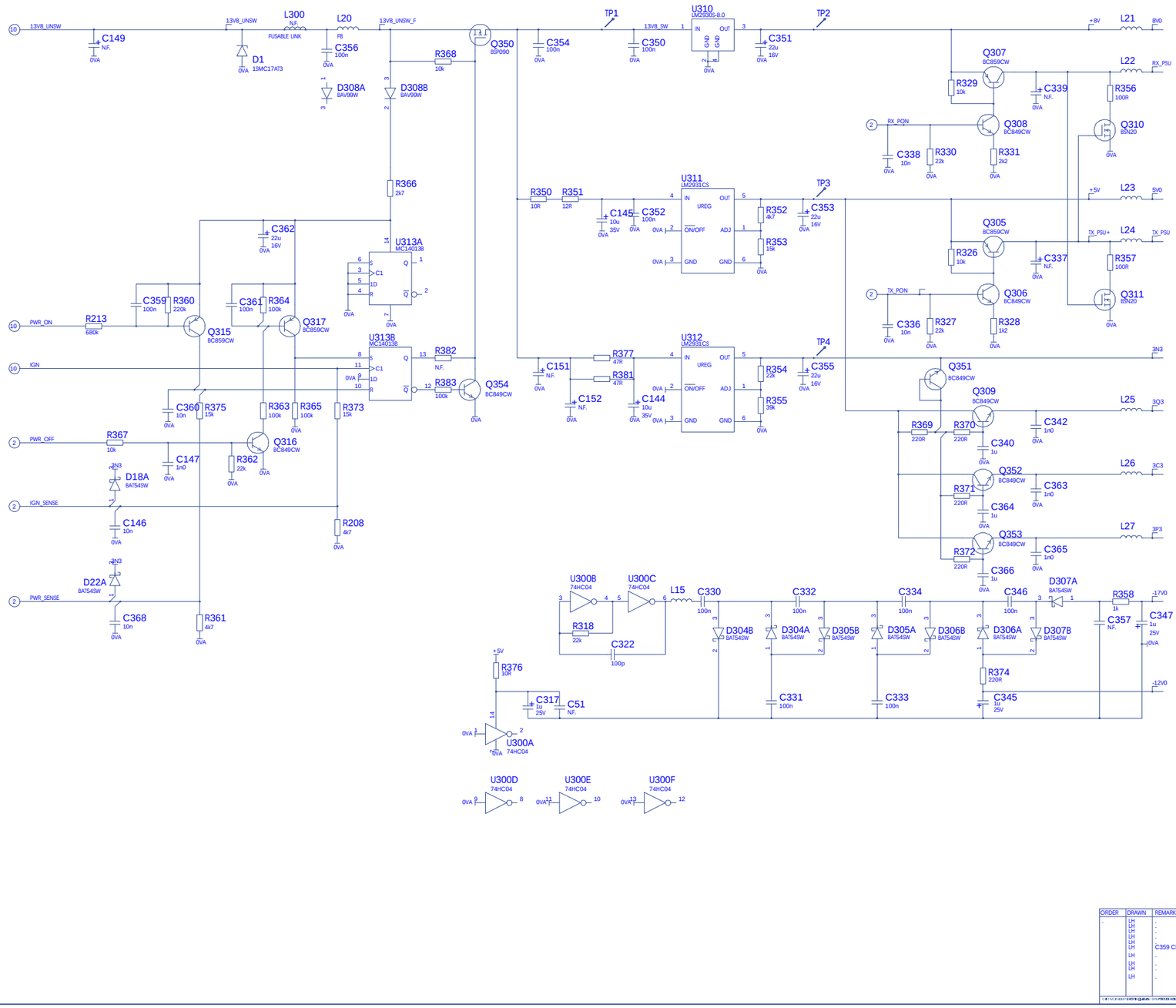


Figure 2 DSP and FGPA Schematic

Issue 7 Circuit Diagrams



ORDER	DRAWN	REMARKS	EXAM	APPD	DATE	REV
-	LH	-	-	JC	5.5.99	1
	LH	-	-	JC	22.7.99	2
	LH	-	-	JC	13.8.99	3
	LH	-	-	JC	17.11.99	4
	LH	-	-	JC	16.12.99	4+
	LH	C359 CNANGE TO 100n	-	JC	17.1.00	4++
	LH	-	-	JC	24.2.00	5
	LH	-	-	JC	15.5.00	6
	LH	-	-	JC	23.6.00	7
	LH	-	-	JC	17.7.00	7A

PROTEL A1

SIMOCO PACIFIC MULGRAVE AUSTRALIA

SRM9000 RADIO BOARD
POWER SUPPLY

CIRCUIT SCHEMATIC

3502 349 6065

130 - 003
010

Figure 3 Power Supply Schematic

Issue 7 Circuit Diagrams

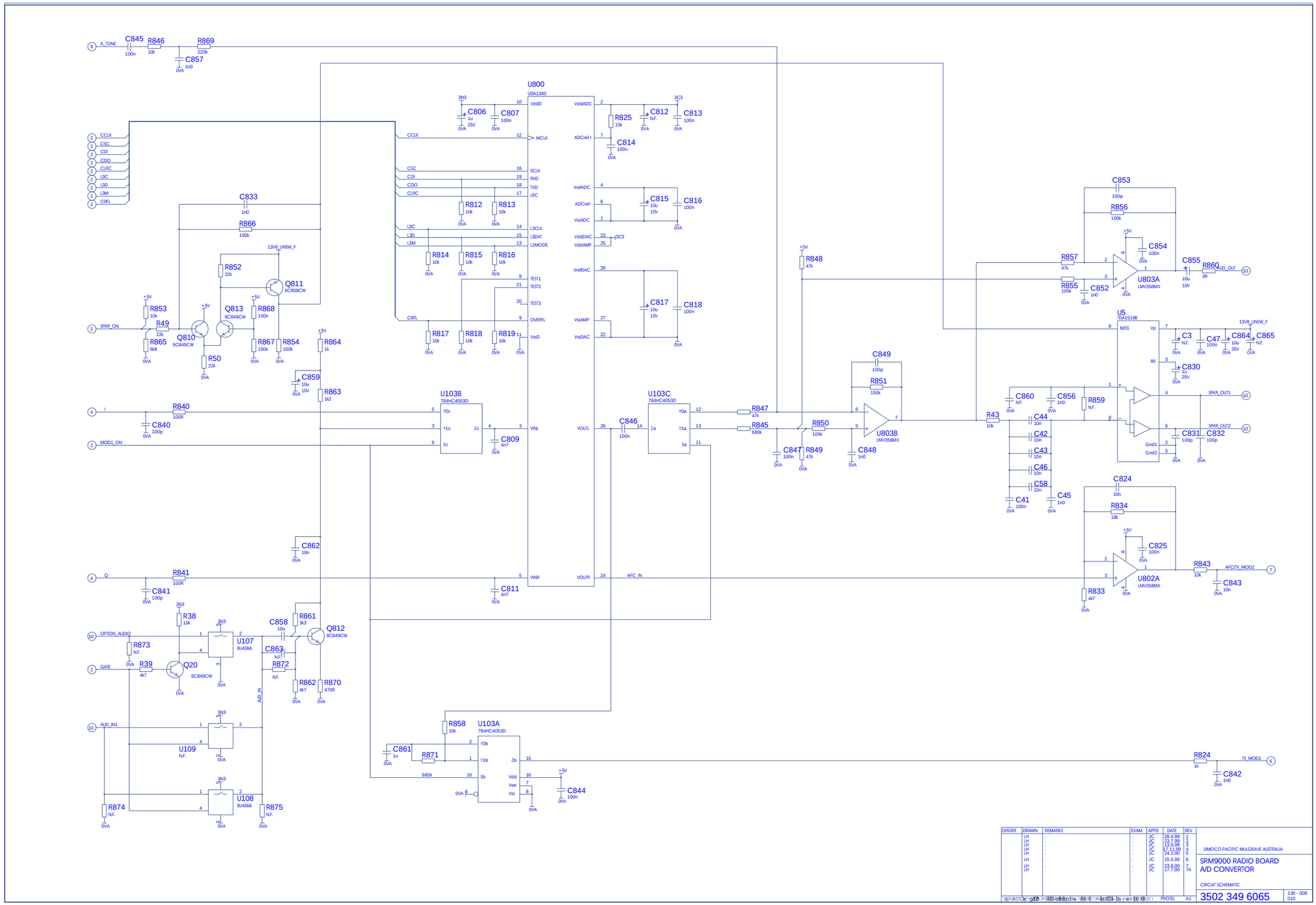
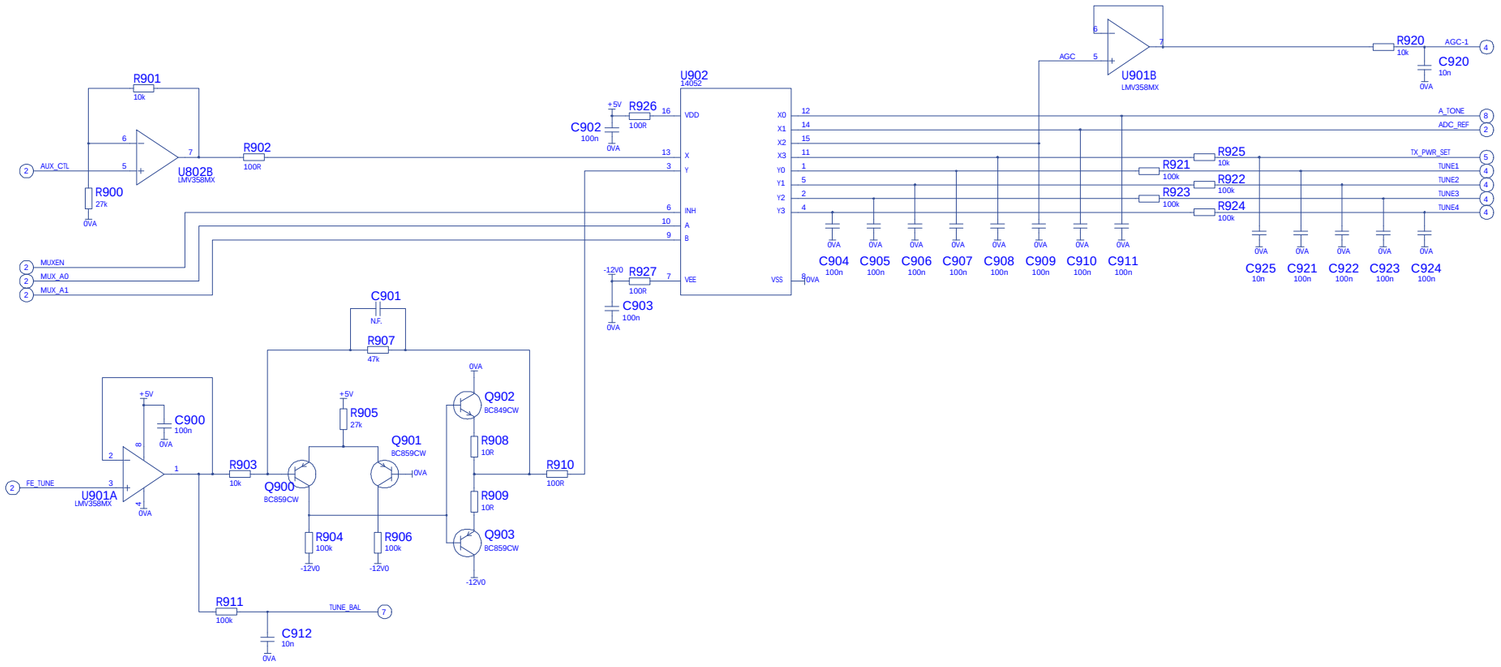


Figure 4 A/D Converter Schematic



ORDER	DRAWN	REMARKS	EXAM	APPD	DATE	REV
	LH	-	-	JC	28.4.99	1
	LH	-	-	JC	23.7.99	2
	LH	-	-	JC	13.8.99	3
	LH	-	-	JC	17.11.99	4
	LH	-	-	JC	16.12.99	4+
	LH	R900 CHANGED FROM 18K TO 27K	-	JC	25.1.00	4++
	LH	-	-	JC	24.2.00	5
	LH	-	-	JC	15.5.00	6
	LH	-	-	JC	23.6.00	7
	LH	-	-	JC	17.7.00	7A

SIMOCO PACIFIC MULGRAVE AUSTRALIA

SRM9000 RADIO BOARD
D/A

CIRCUIT SCHEMATIC

3502 349 6065 | 130 - 009 010

PROTEL | A1

Figure 5 D/A Multiplexer Schematic

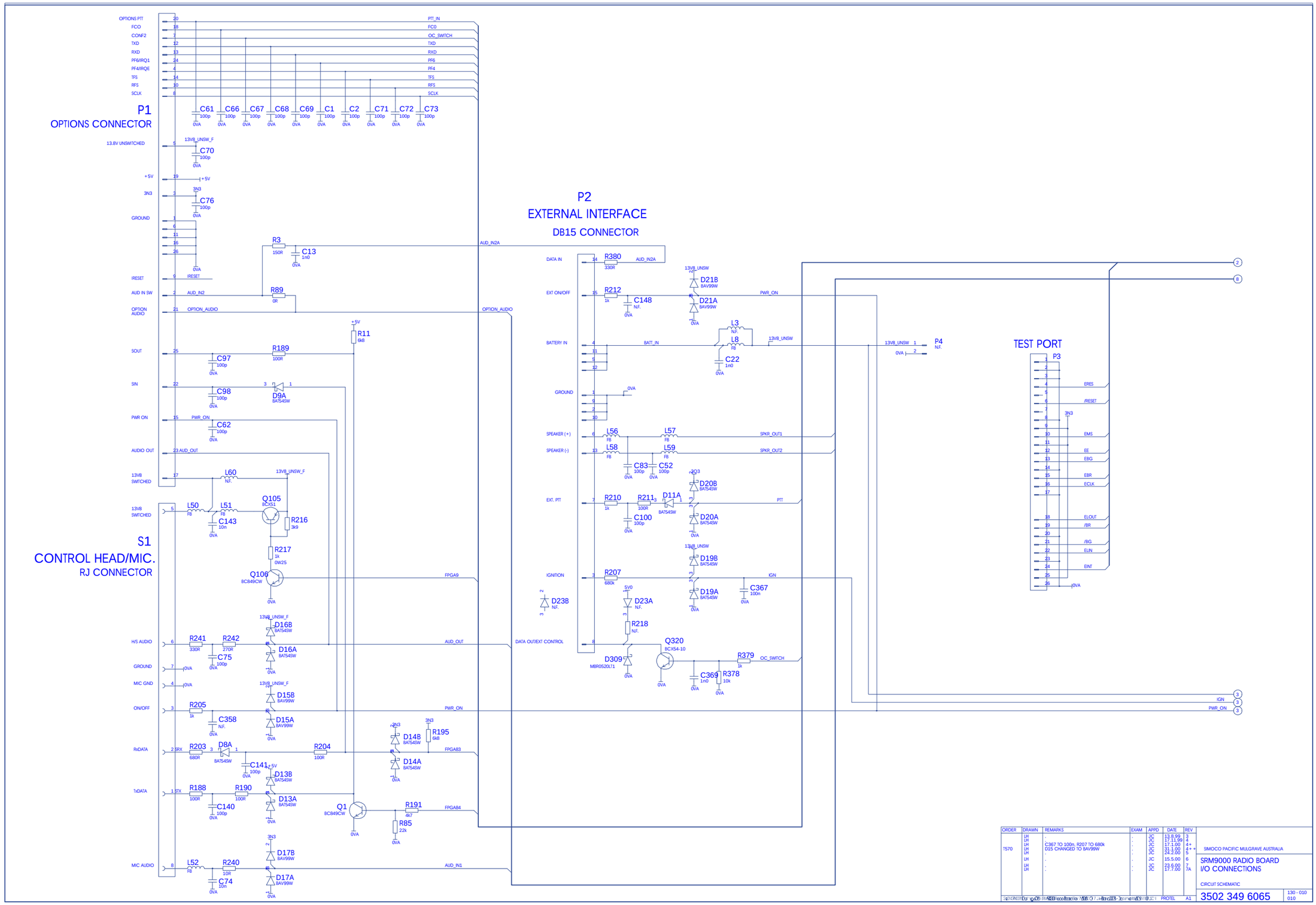


Figure 6 I/O Connections Schematic

Issue 7 Circuit Diagrams

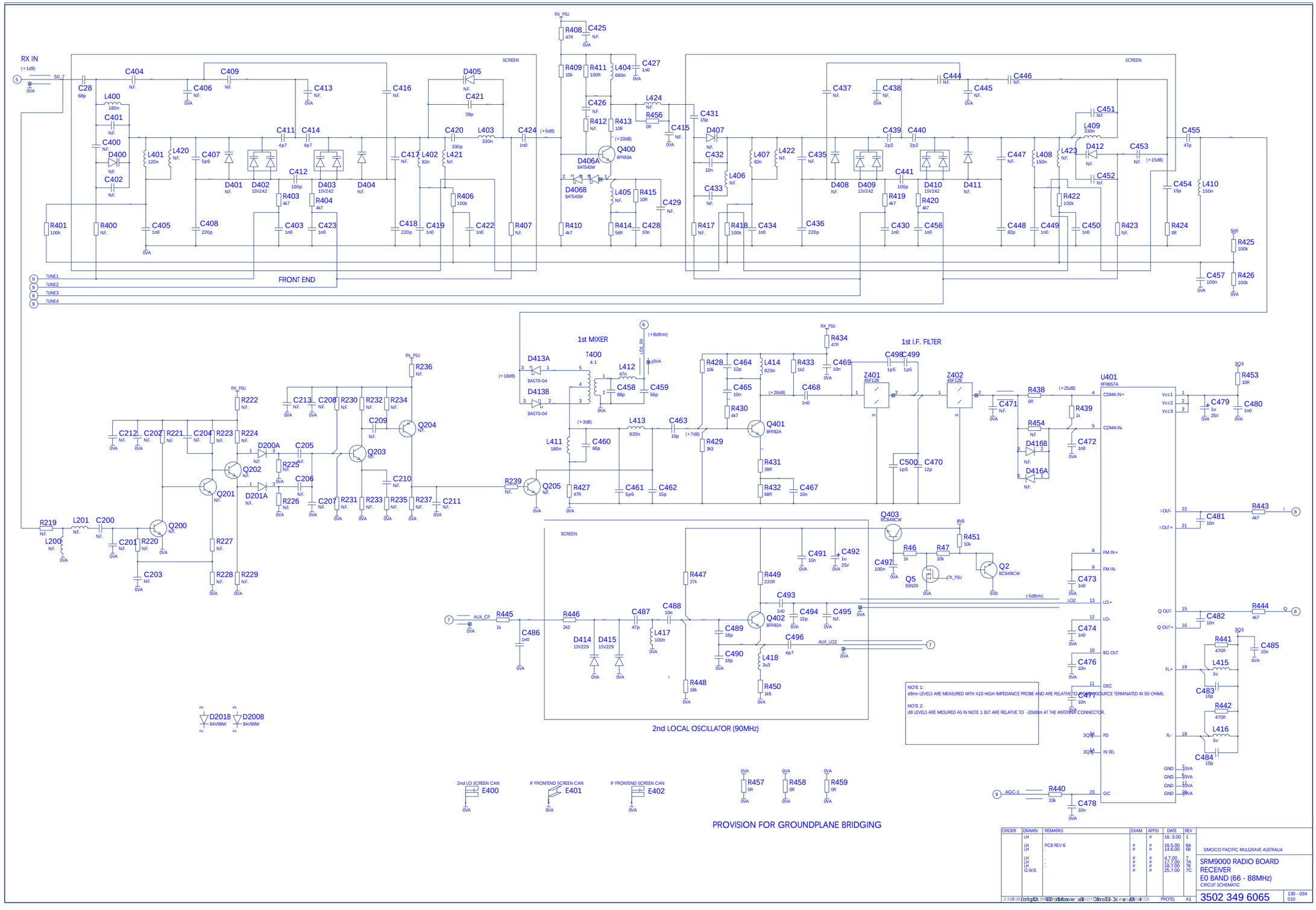


Figure 7 Reciever (EO Band) Schematic

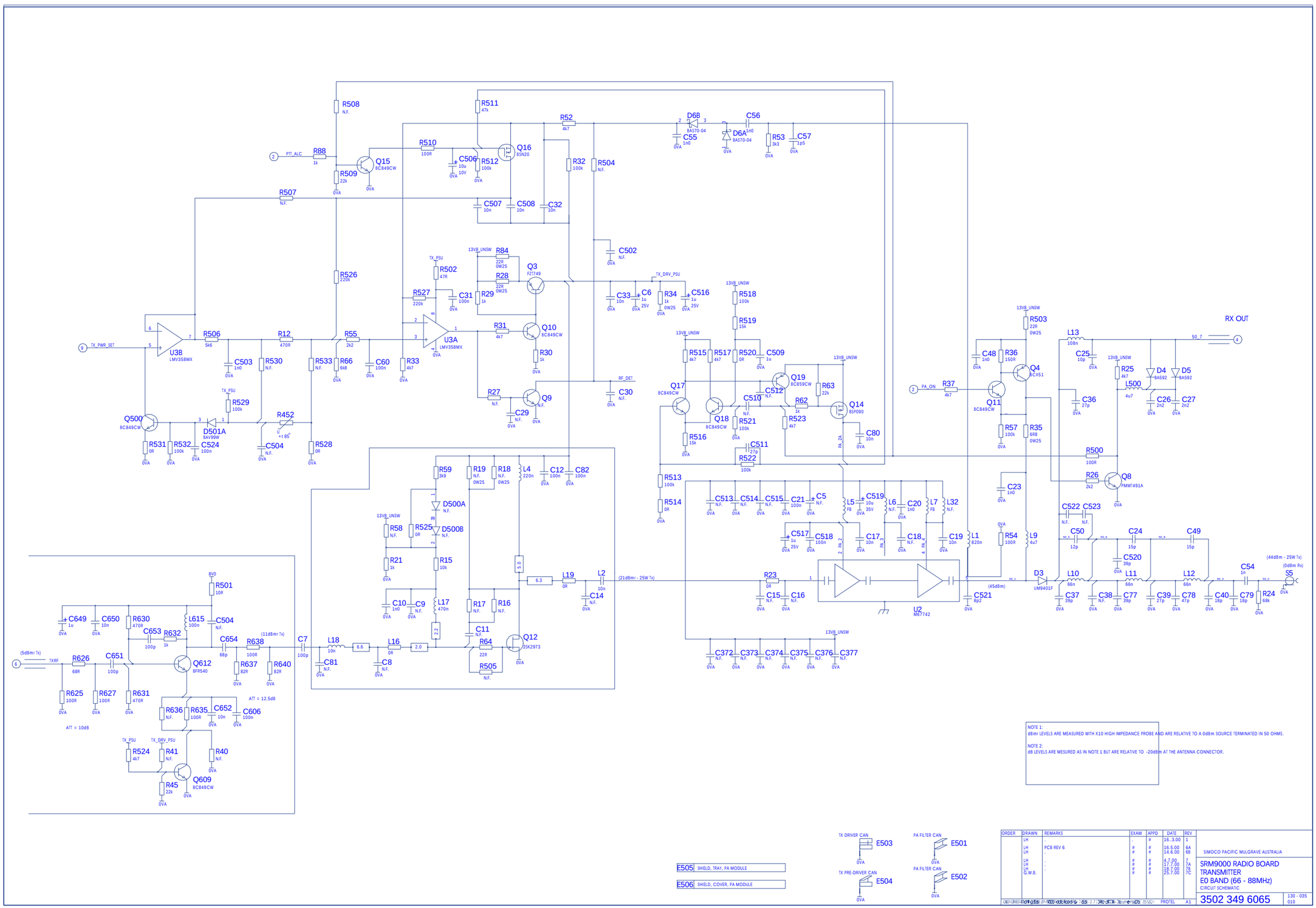


Figure 8 Transmitter (EO Band) Schematic

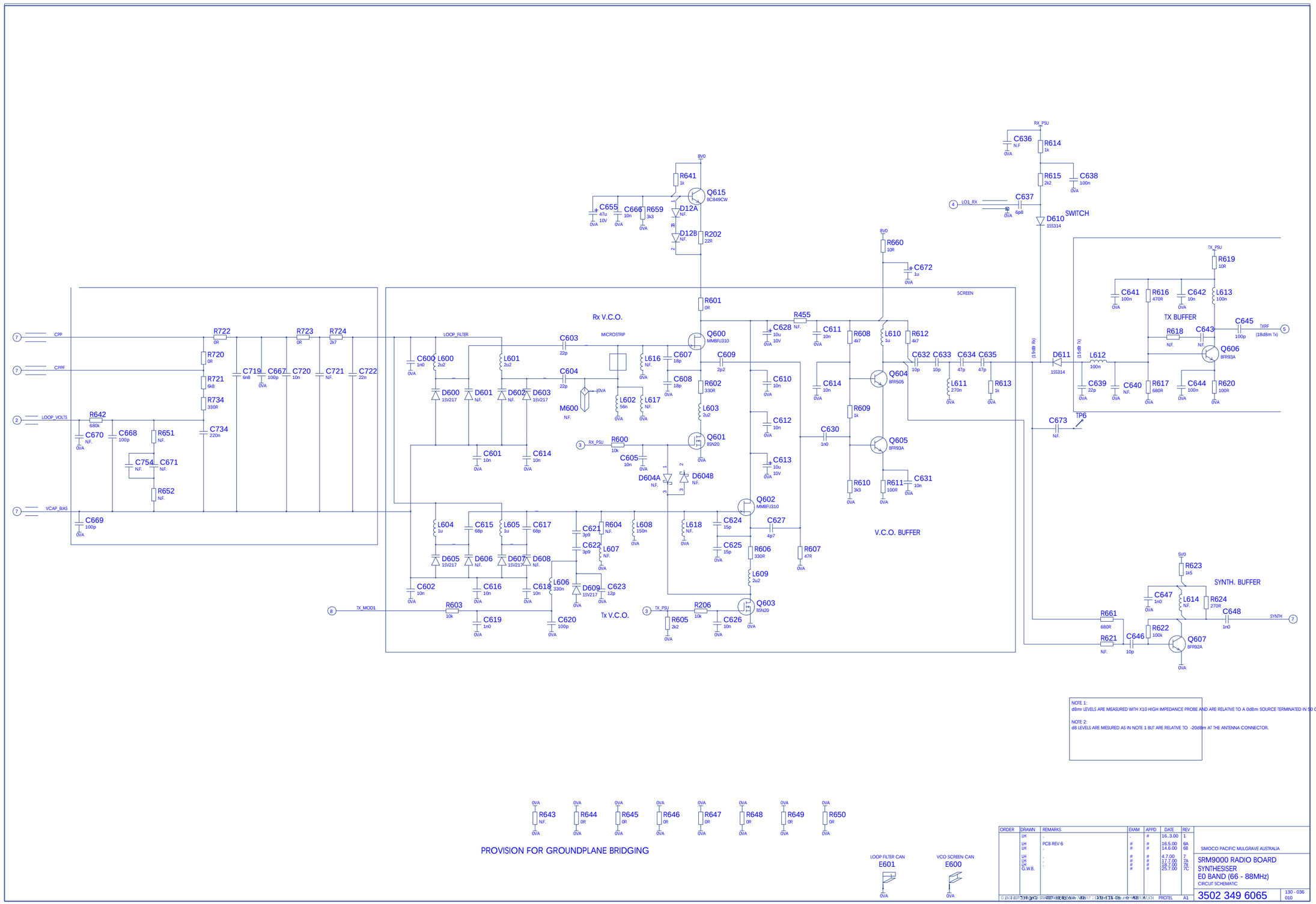


Figure 9 Synthesiser VCO (EO Band) Schematic

Issue 7 Circuit Diagrams

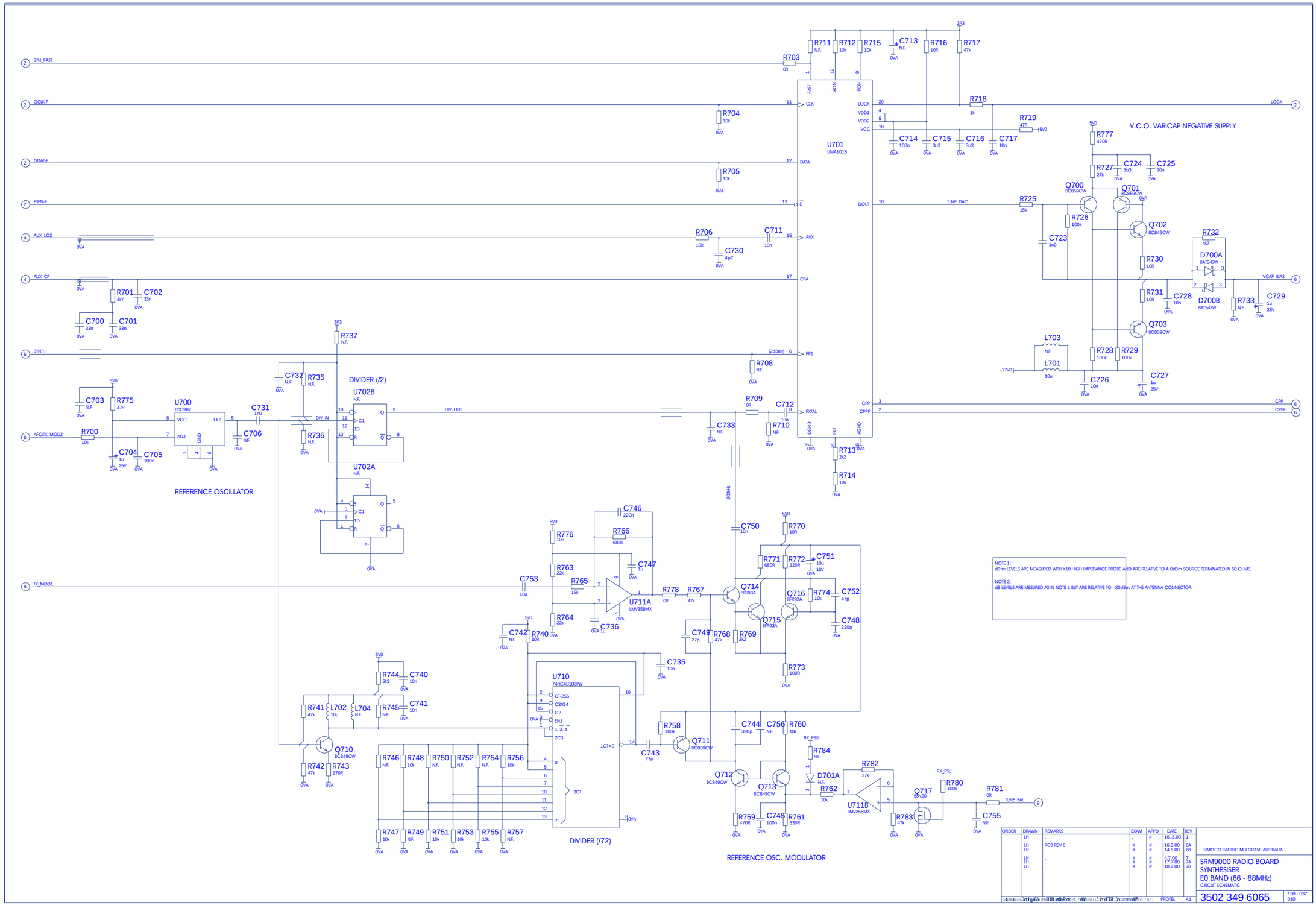


Figure 10 Synthesiser PLL (EO Band) Schematic

Issue 7 Circuit Diagrams

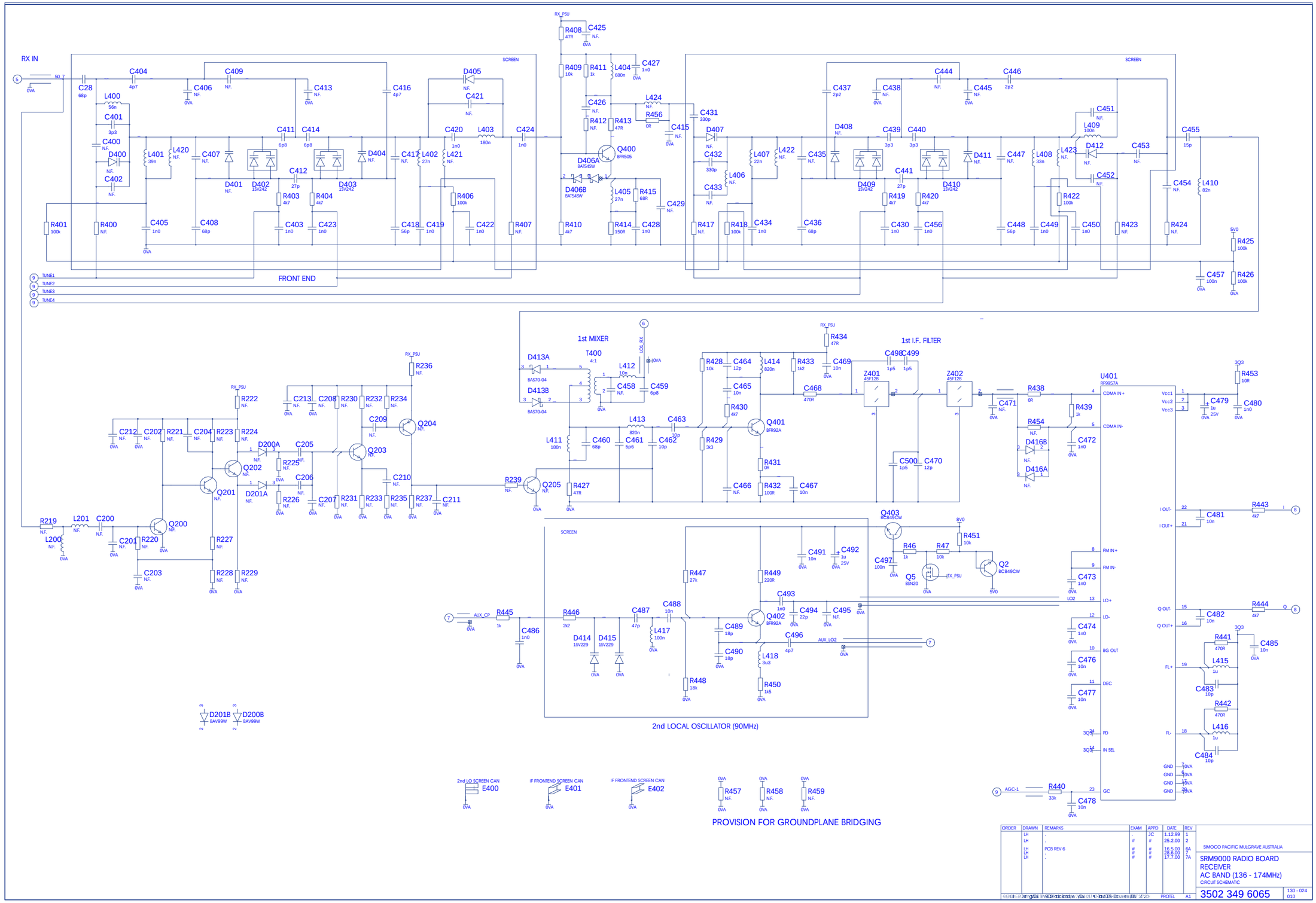


Figure 11 Receiver (AC Band) Schematic

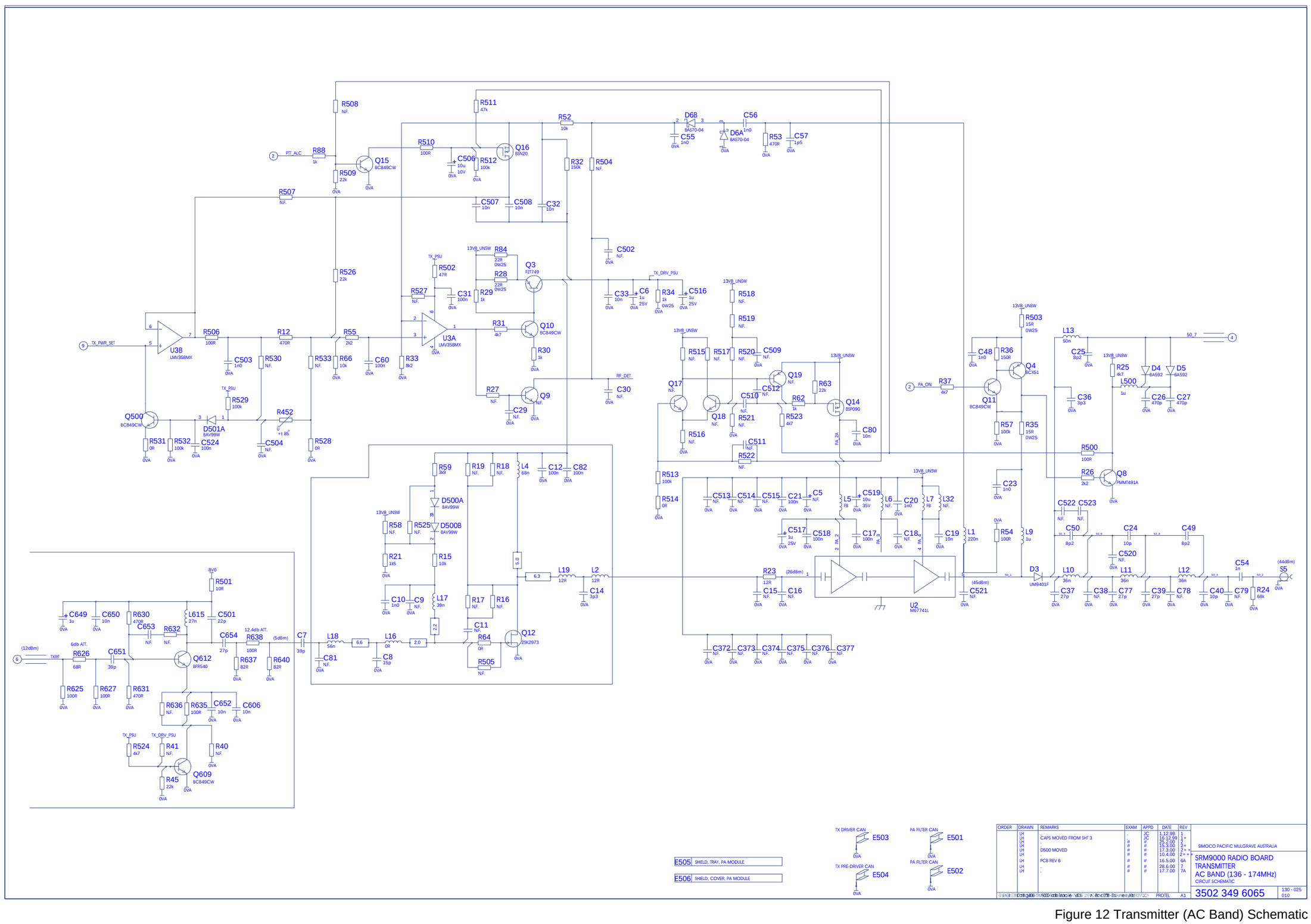


Figure 12 Transmitter (AC Band) Schematic

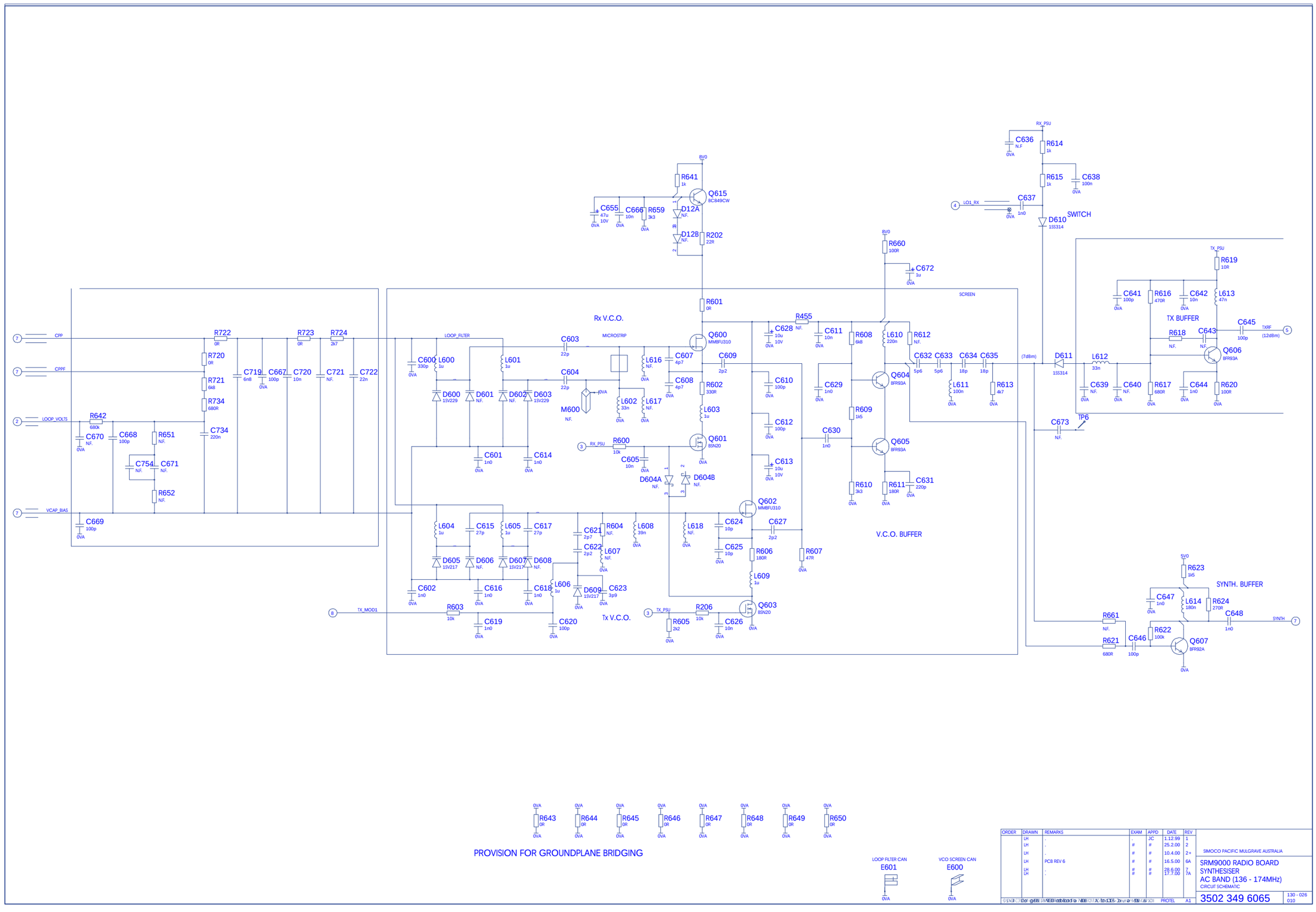


Figure 13 Synthesiser VCO (AC Band) Schematic

Issue 7 Circuit Diagrams

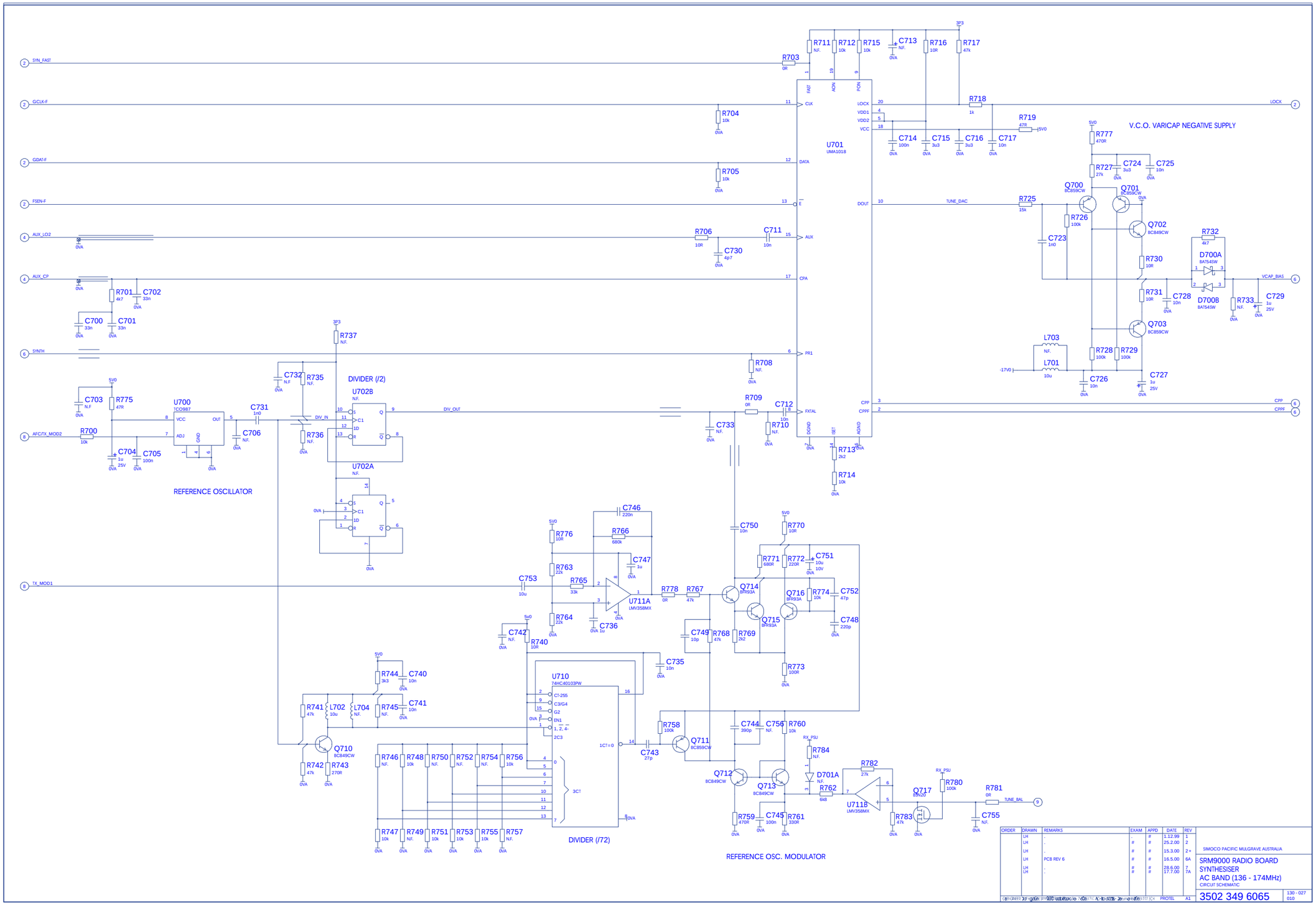


Figure 14 Synthesiser PLL (AC Band) Schematic

Issue 7 Circuit Diagrams

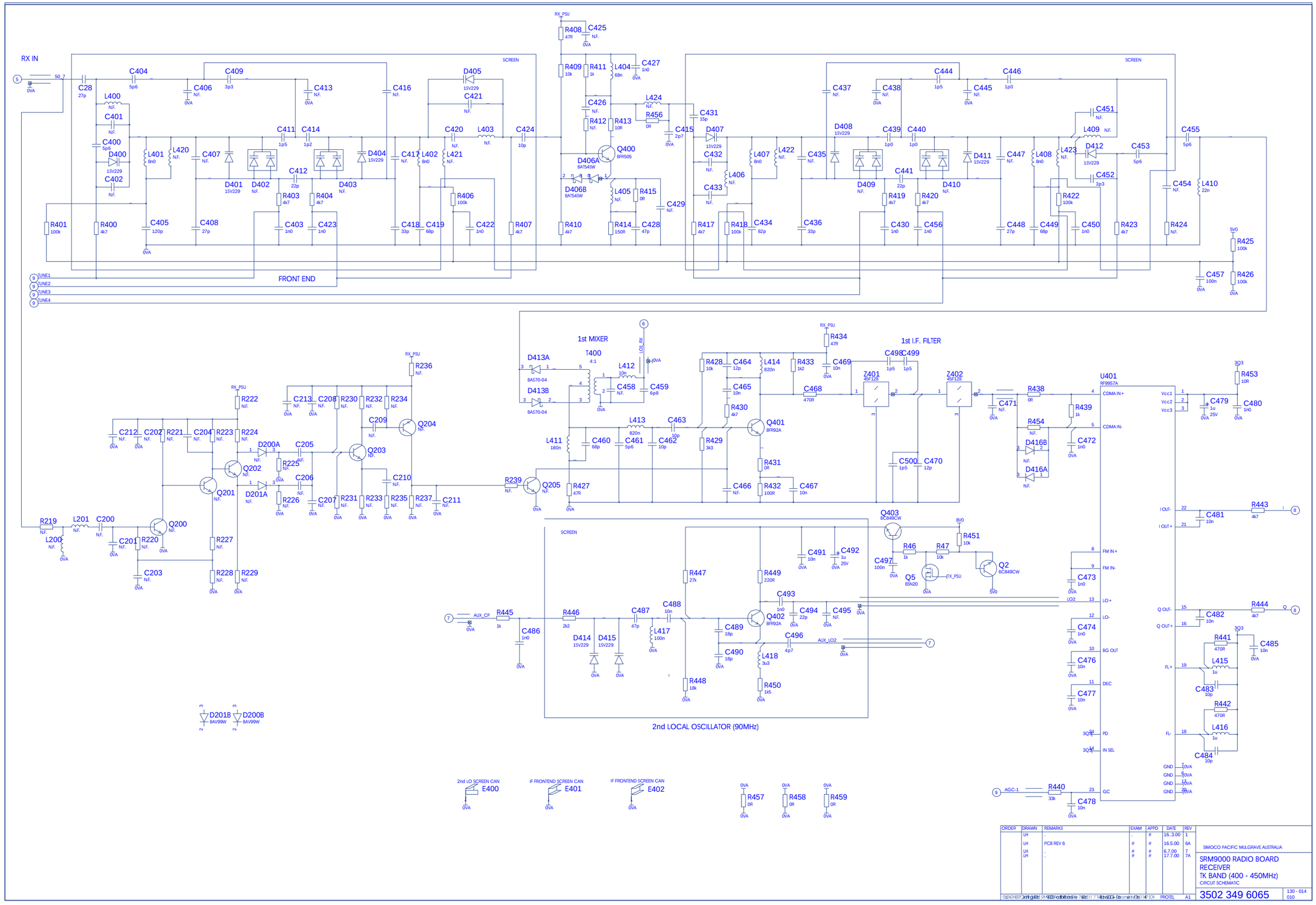


Figure 15 Receiver (TK Band) Schematic

Issue 7 Circuit Diagrams

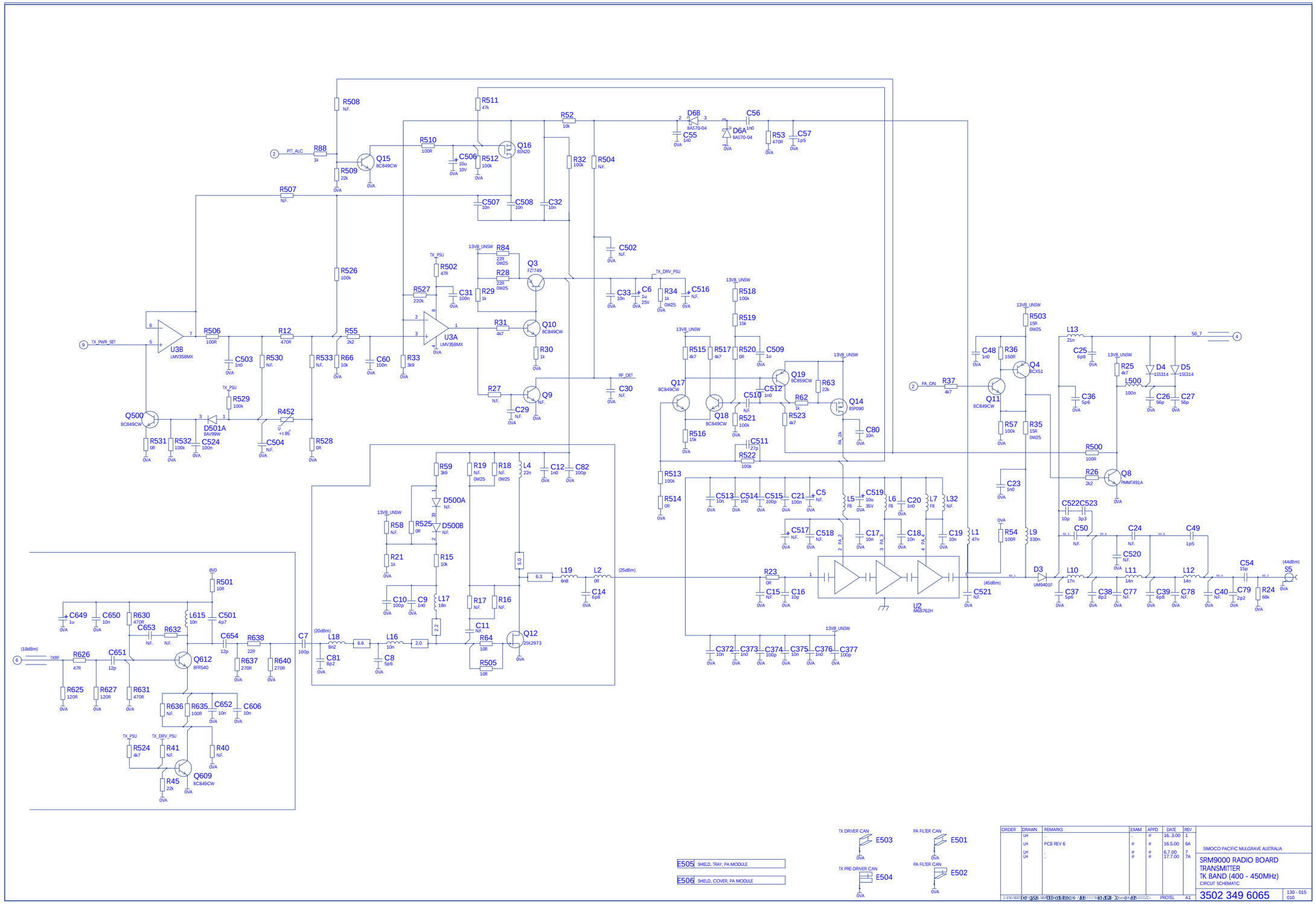


Figure 16 Transmitter (TK Band) Schematic

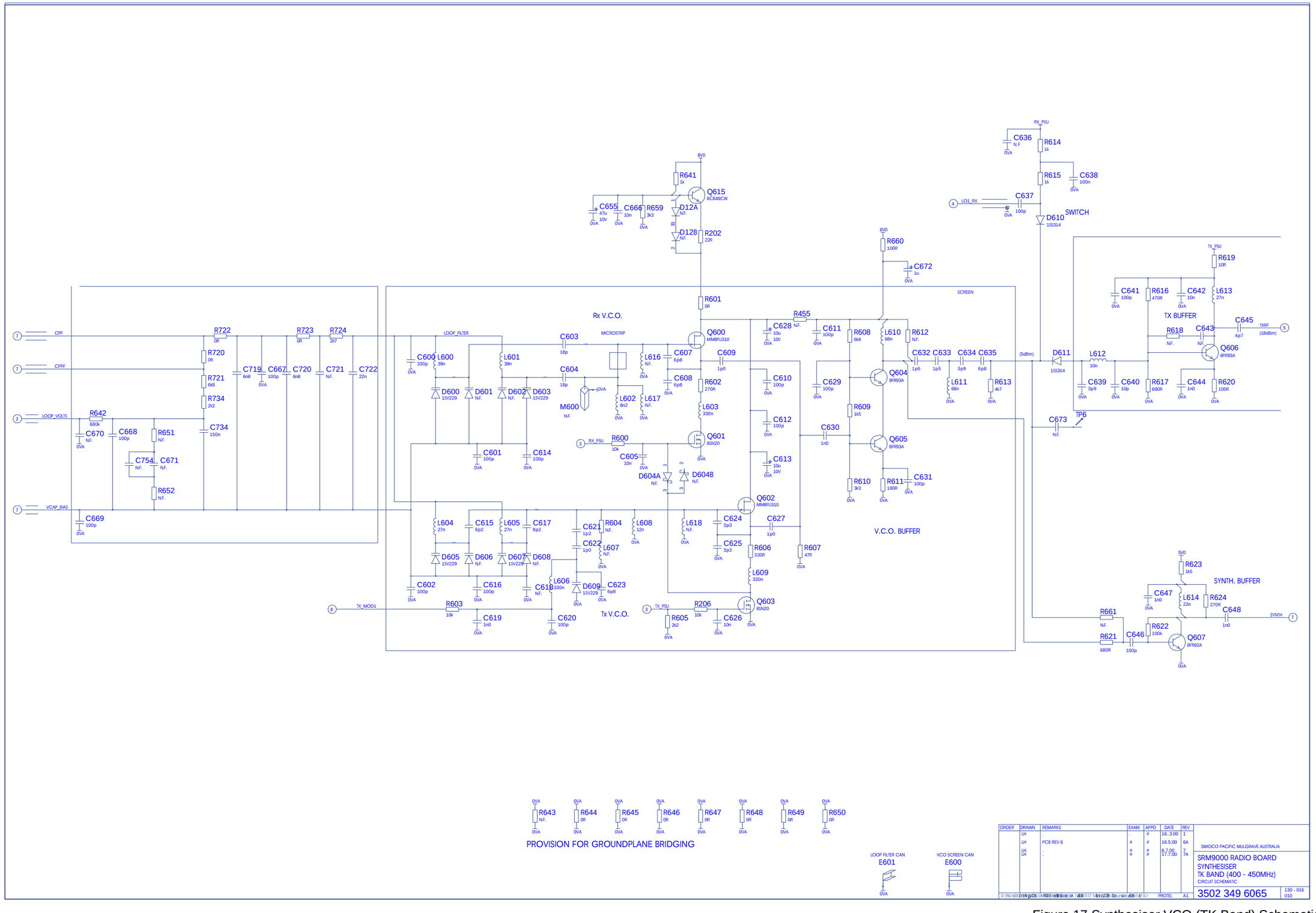


Figure 17 Synthesiser VCO (TK Band) Schematic

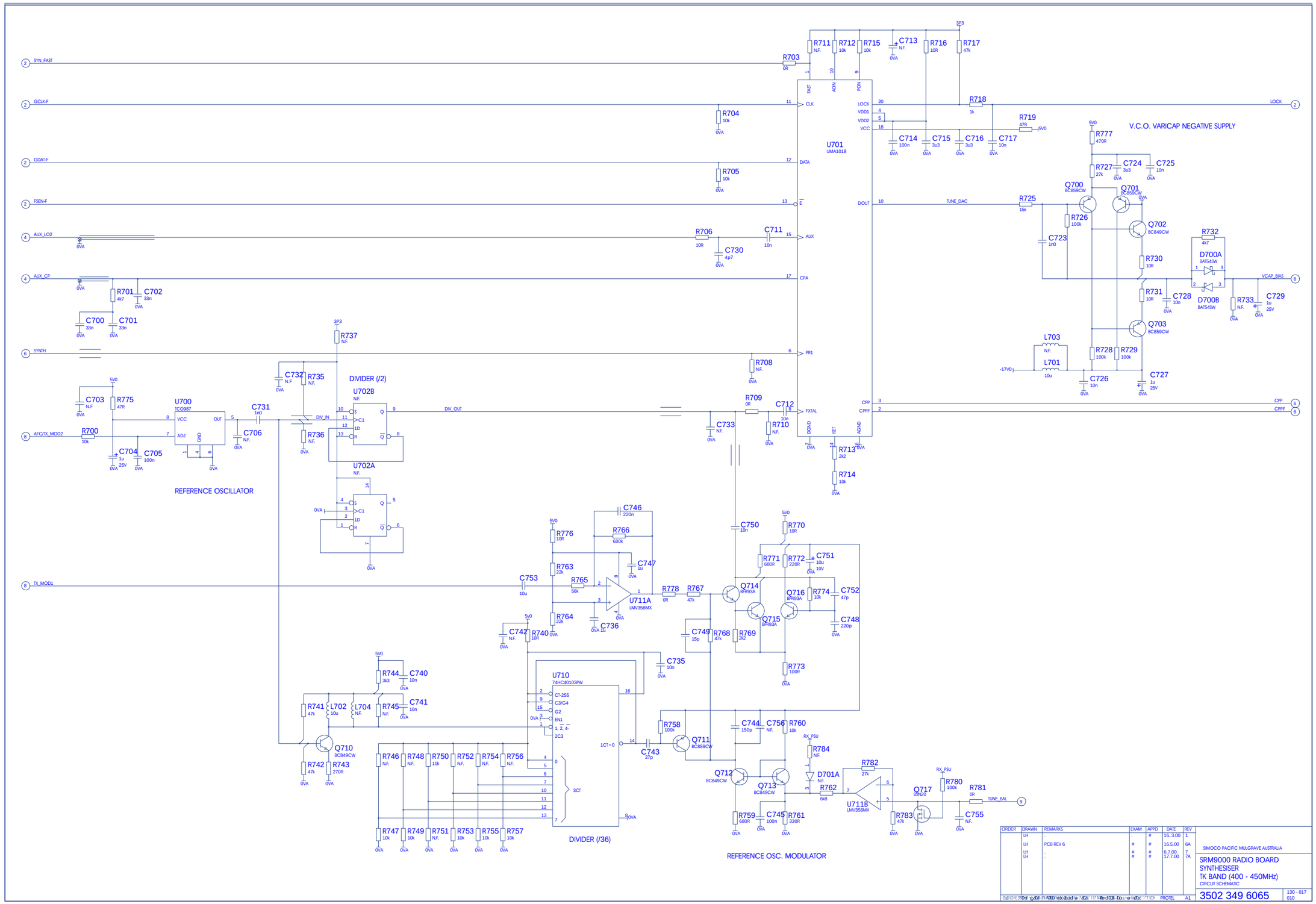


Figure 18 Synthesiser PLL (TK Band) Schematic

Issue 7 Circuit Diagrams

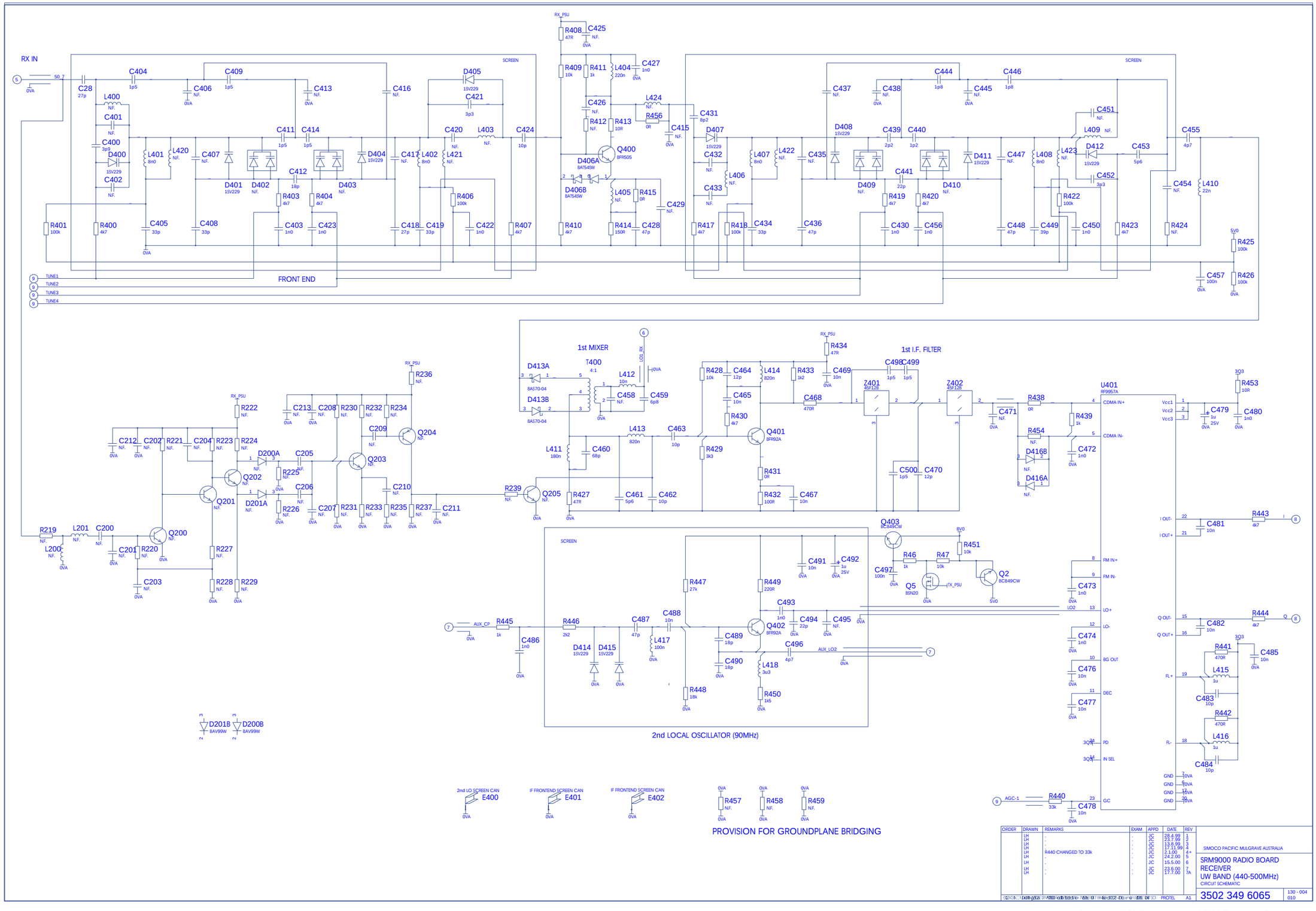


Figure 19 Receiver (UW Band) Schematic

Issue 7 Circuit Diagrams

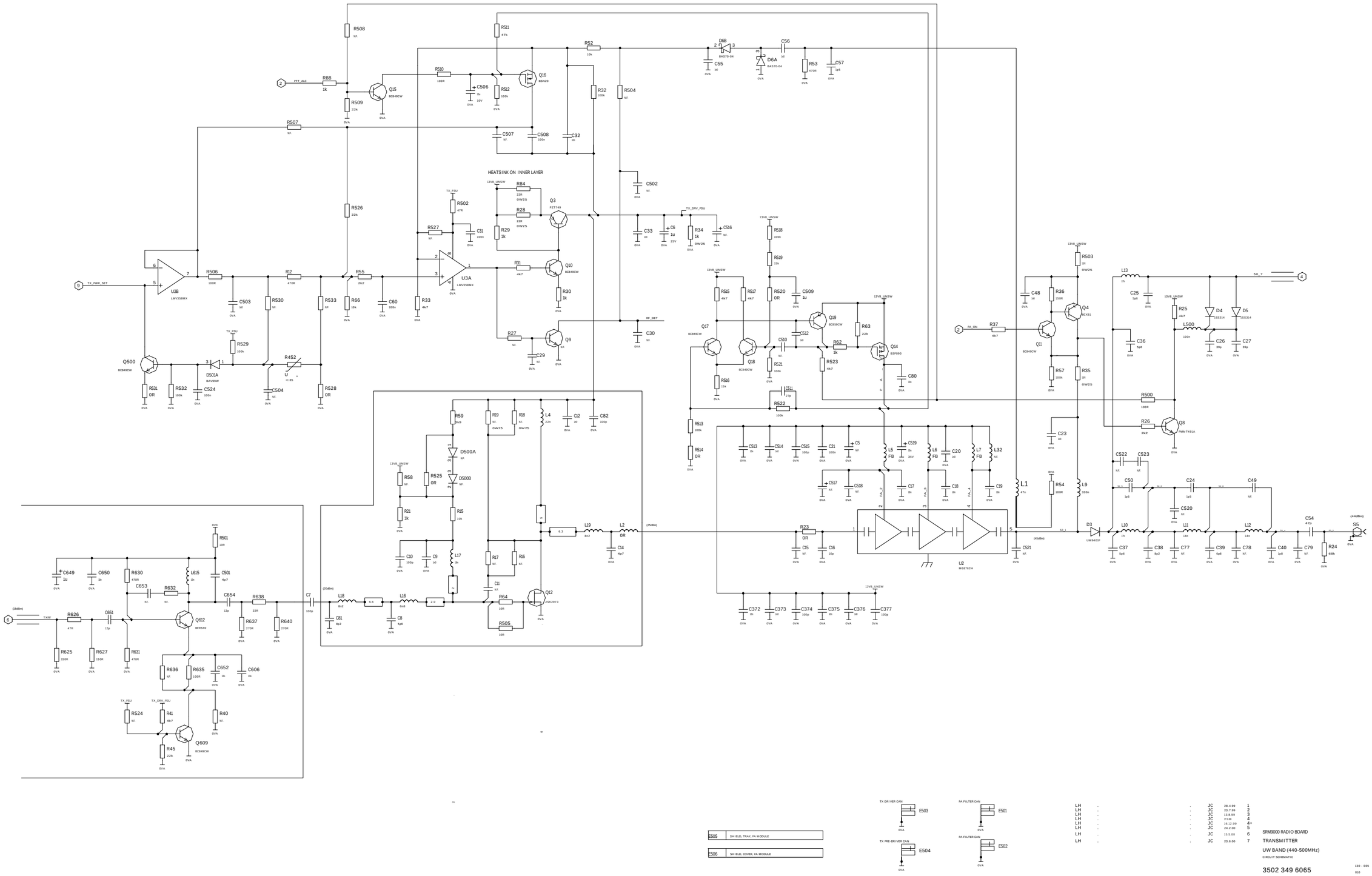


Figure 20 Transmitter (UW Band) Schematic

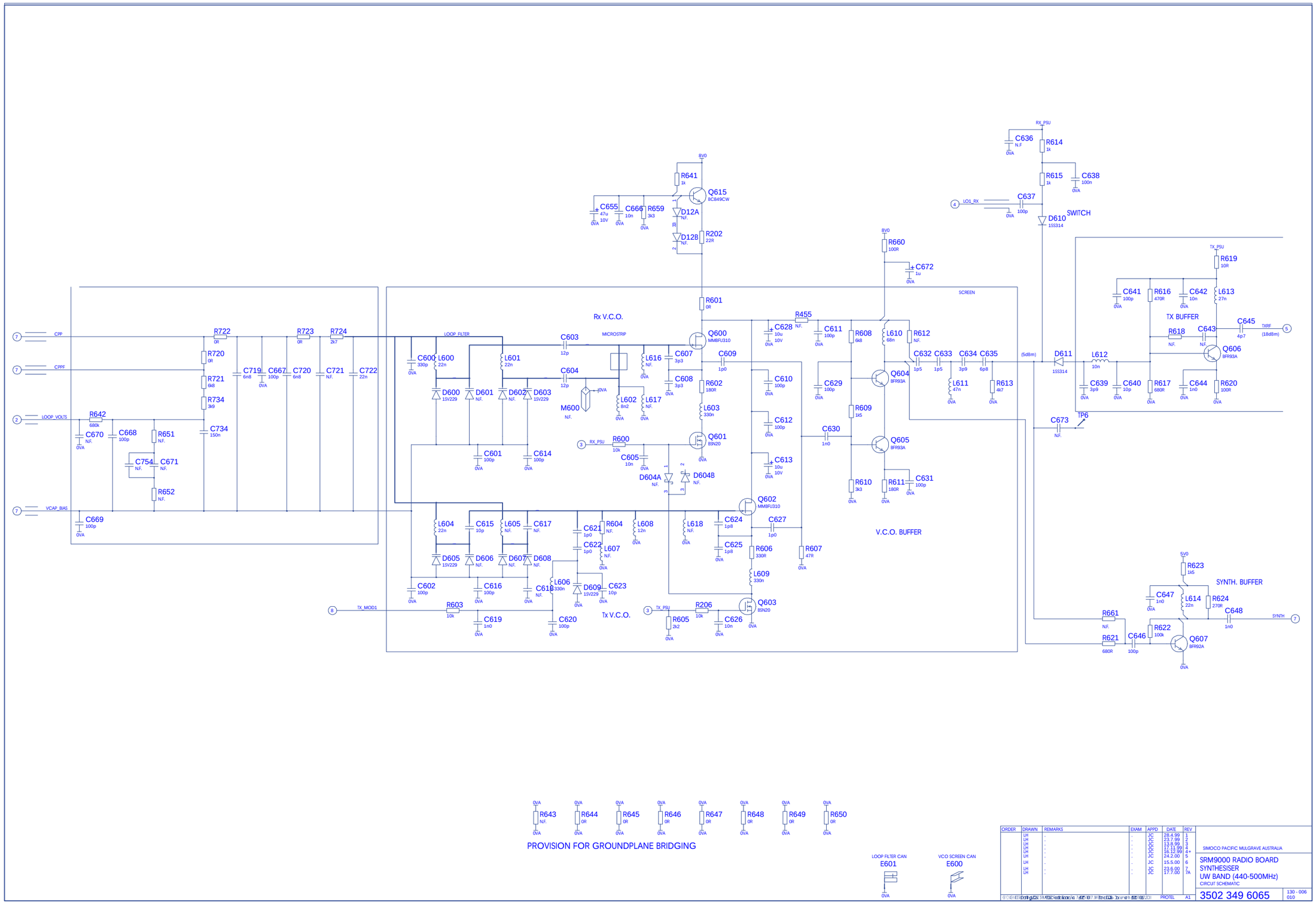


Figure 21 Synthesiser VCO (UW Band) Schematic

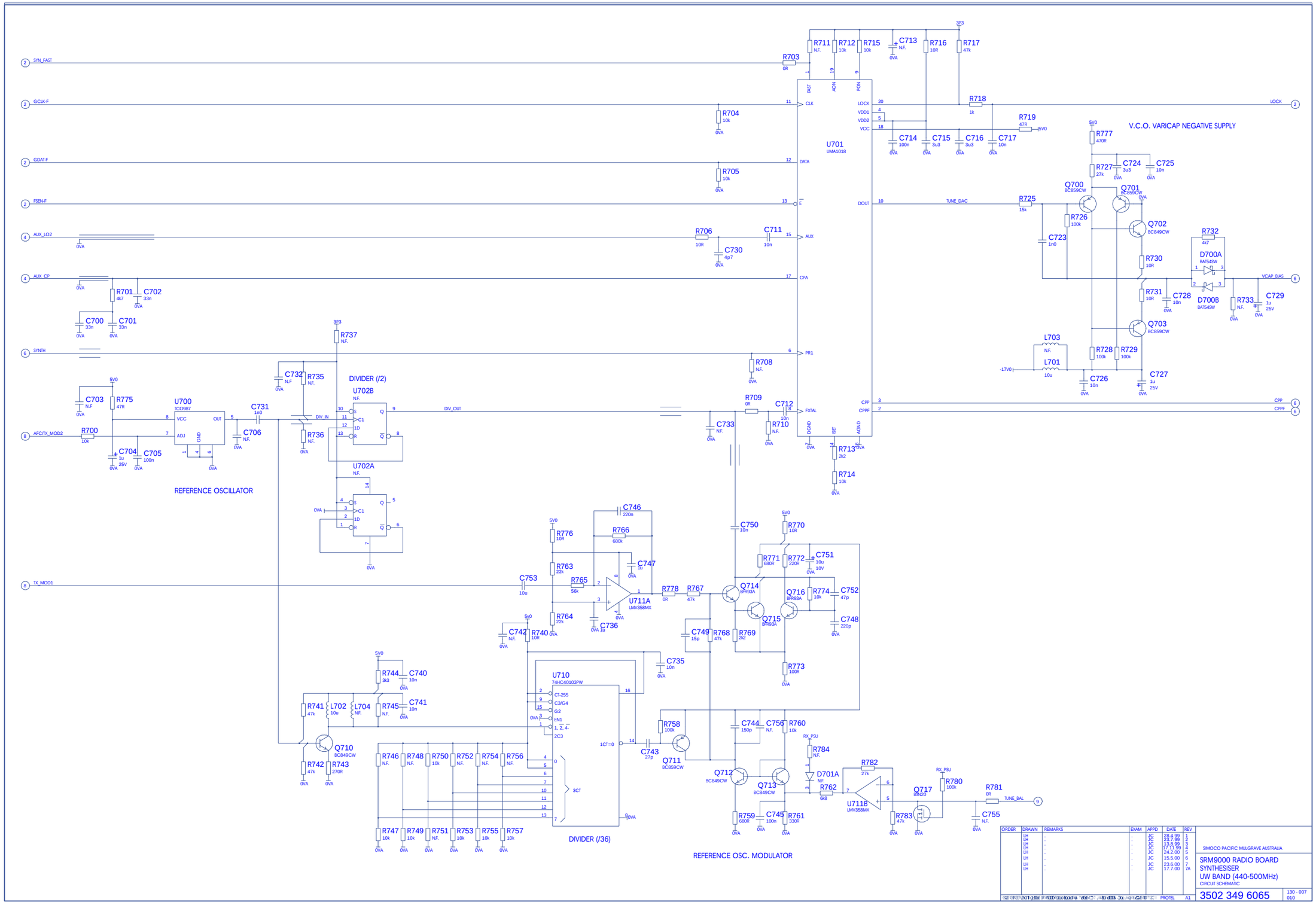


Figure 22 Synthesiser PLL (UW Band) Schematic

Issue 7 Circuit Diagrams

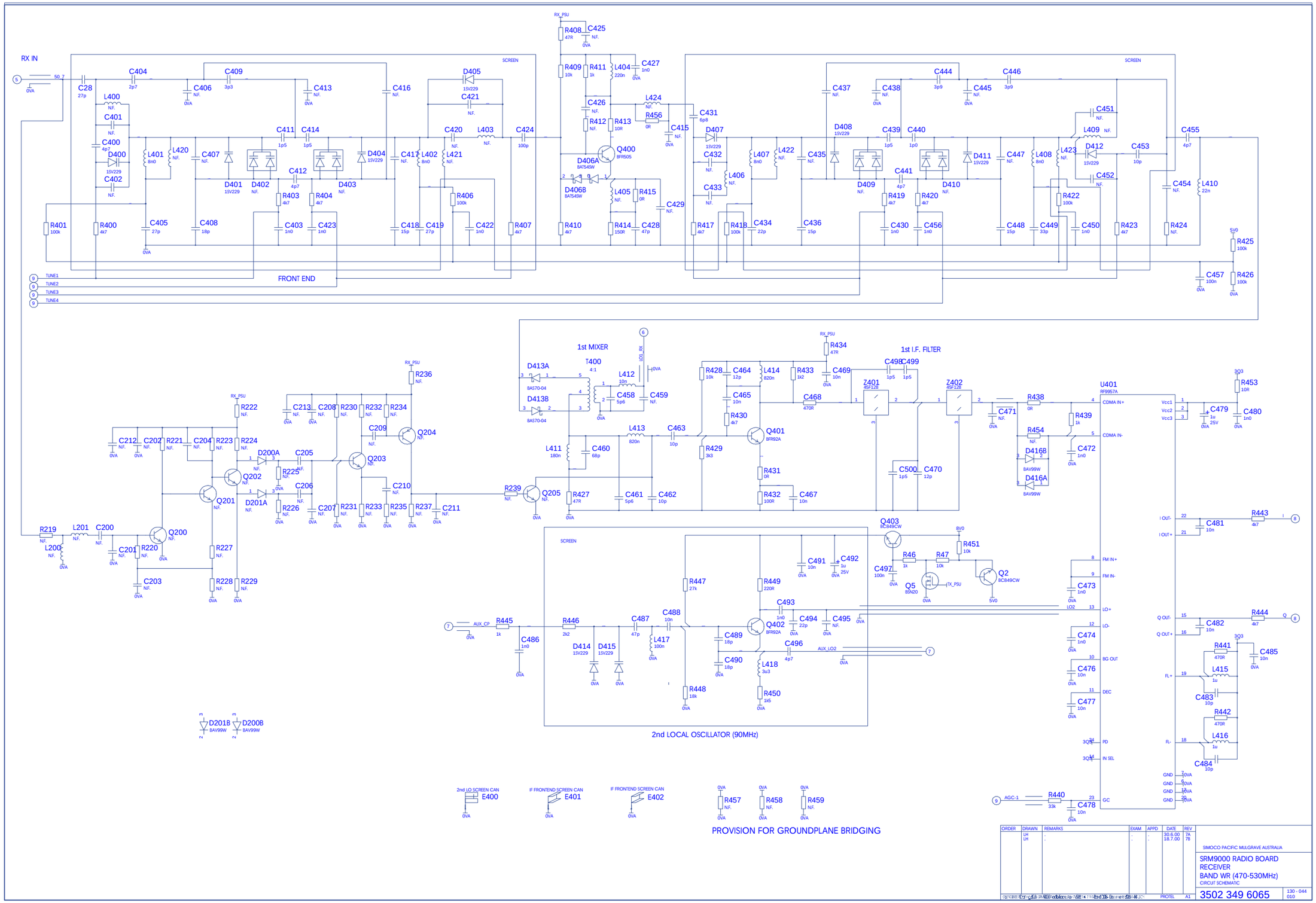


Figure 23 Receiver (WR Band) Schematic

Issue 7 Circuit Diagrams

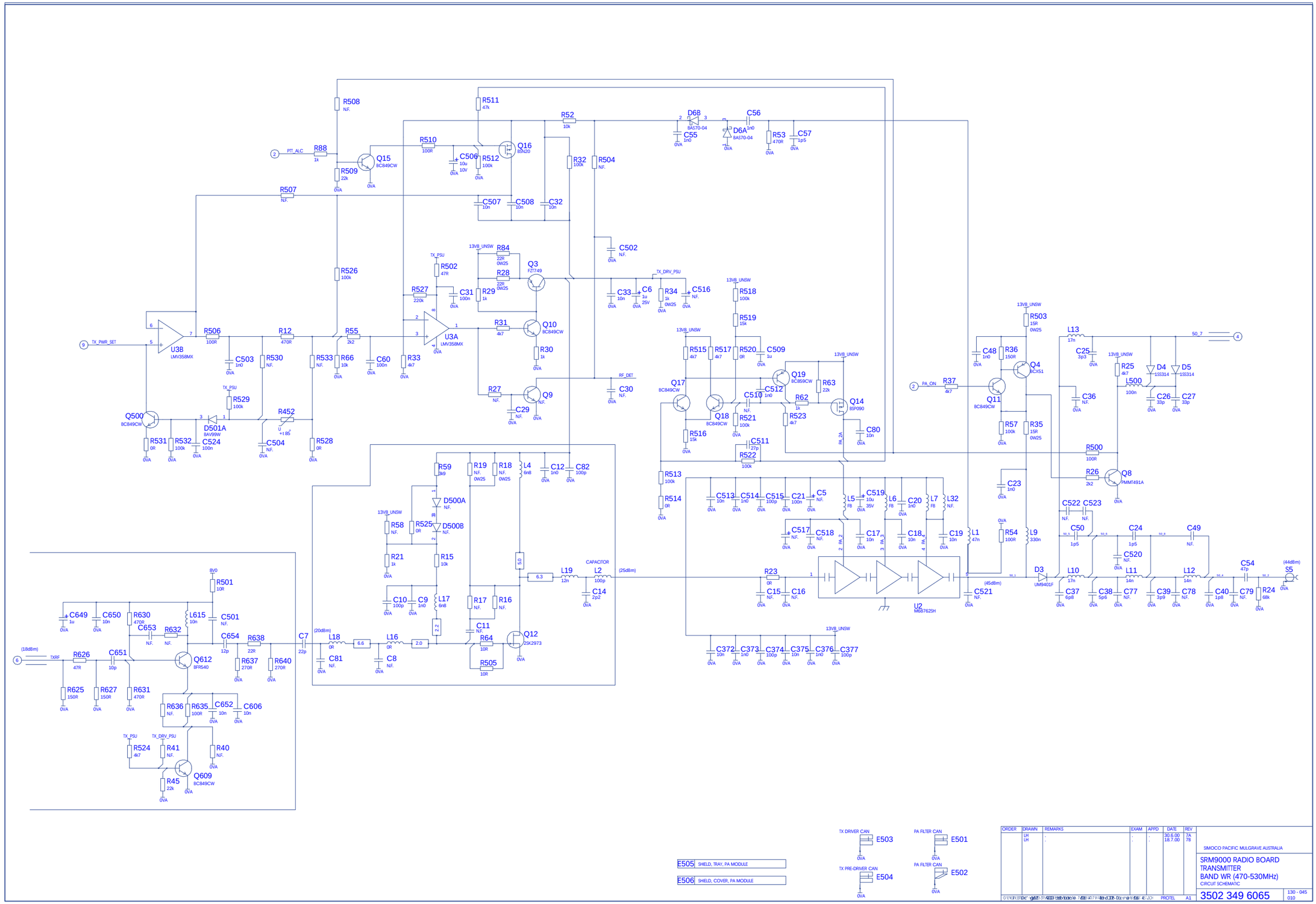


Figure 24 Transmitter (WR Band) Schematic

Issue 7 Circuit Diagrams

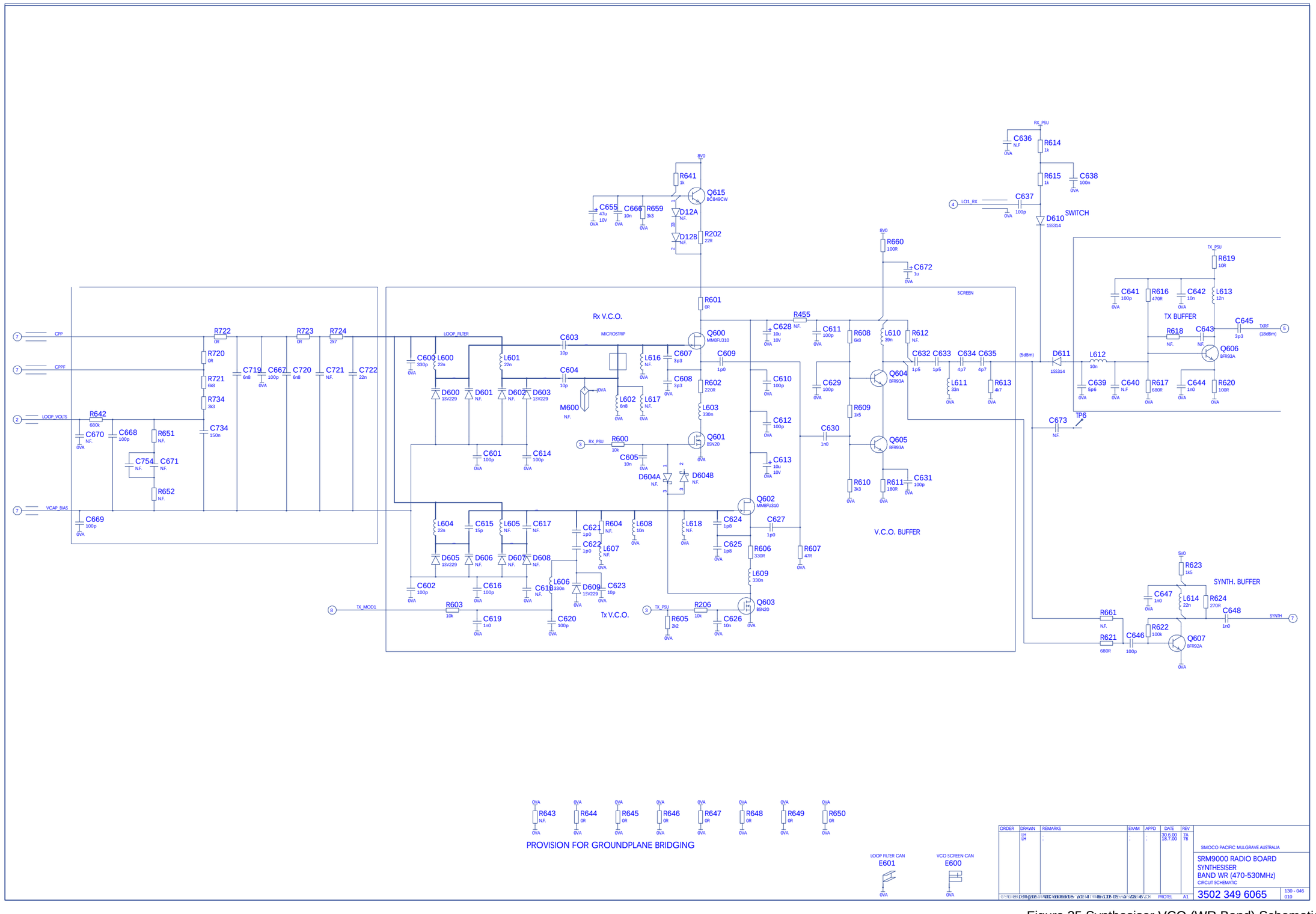


Figure 25 Synthesiser VCO (WR Band) Schematic

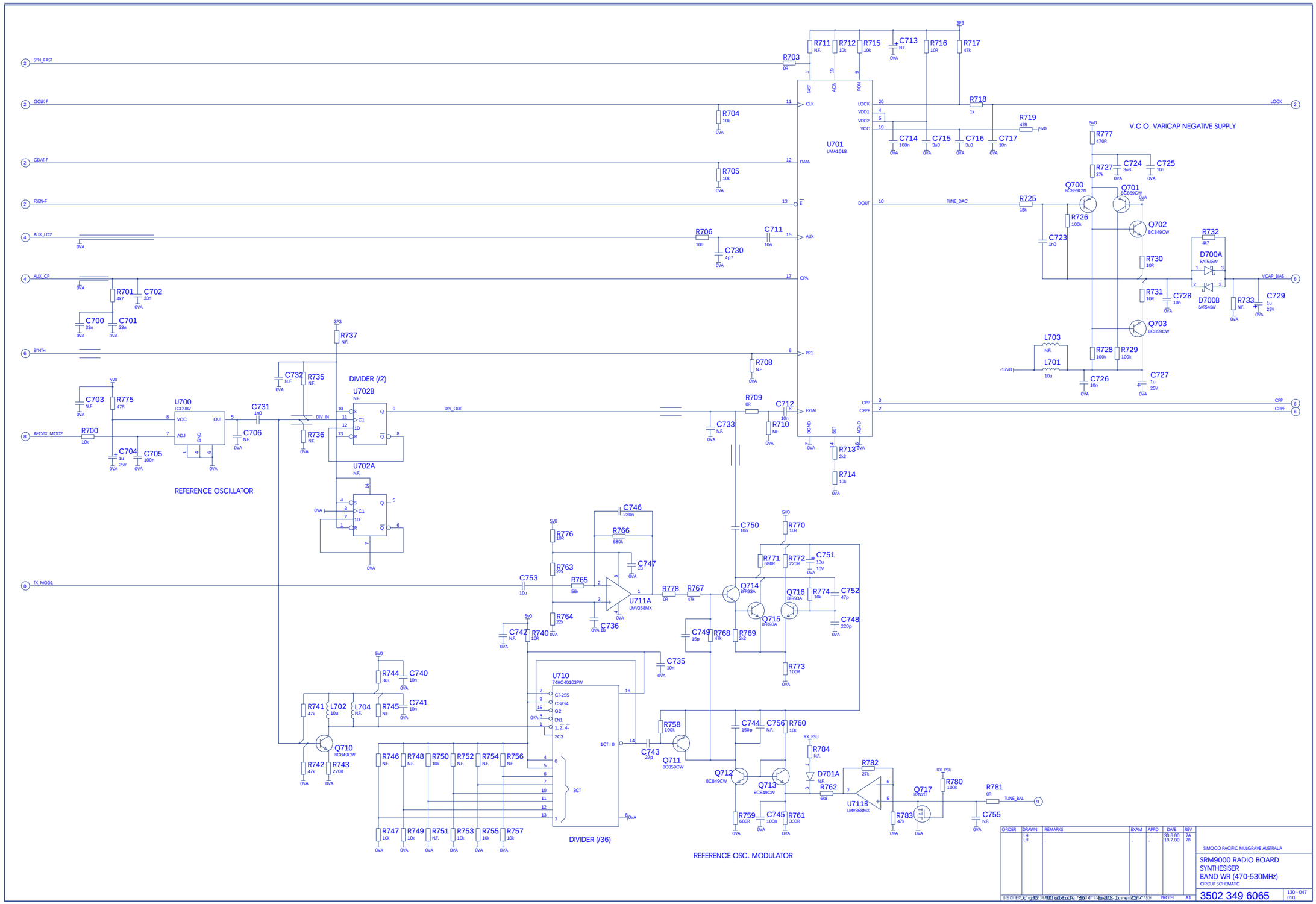


Figure 26 Synthesiser PLL (WR Band) Schemati

Issue 7 Circuit Diagrams

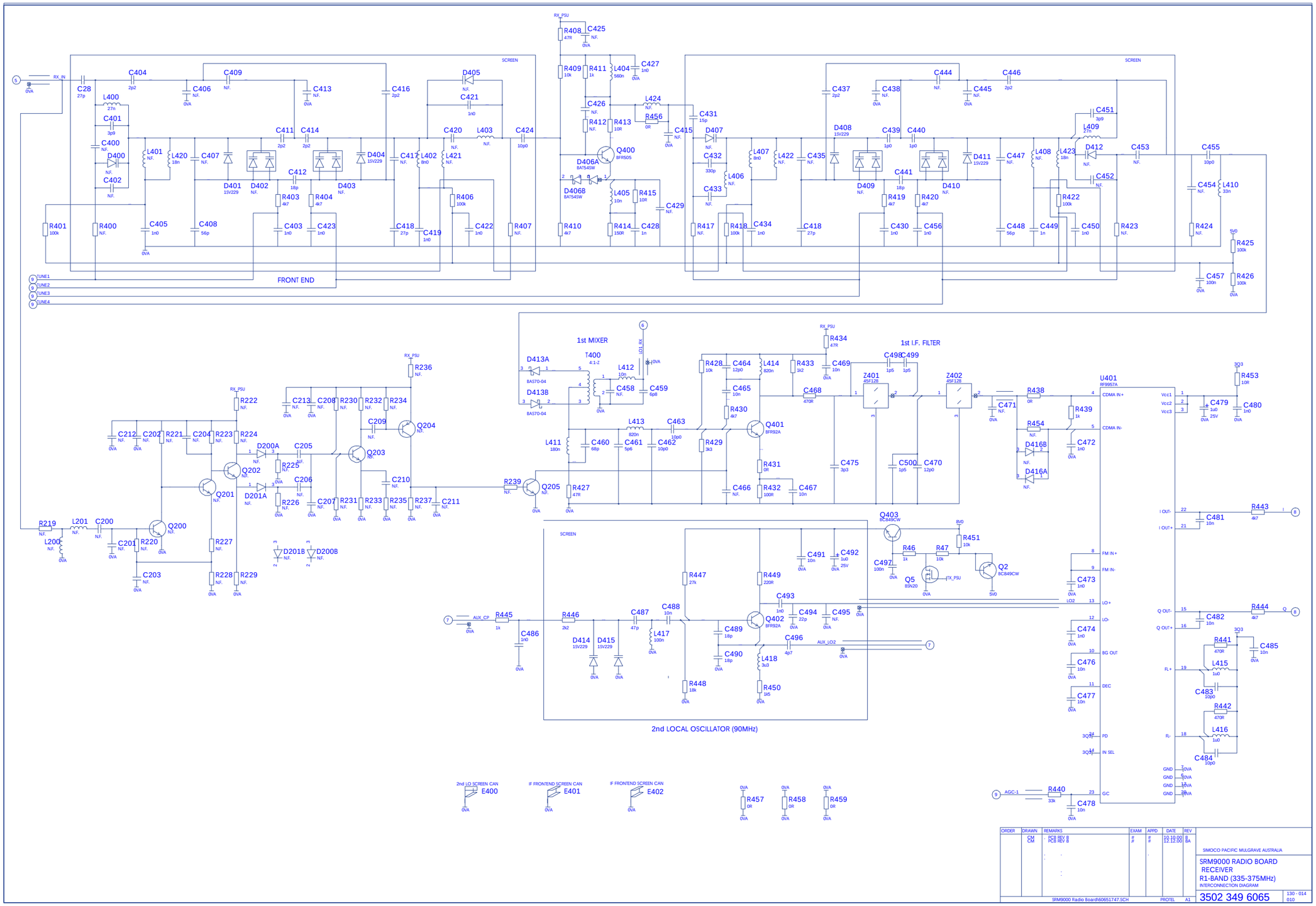


Figure 27 Receiver (R1 Band) Schematic

Issue 7 Circuit Diagrams

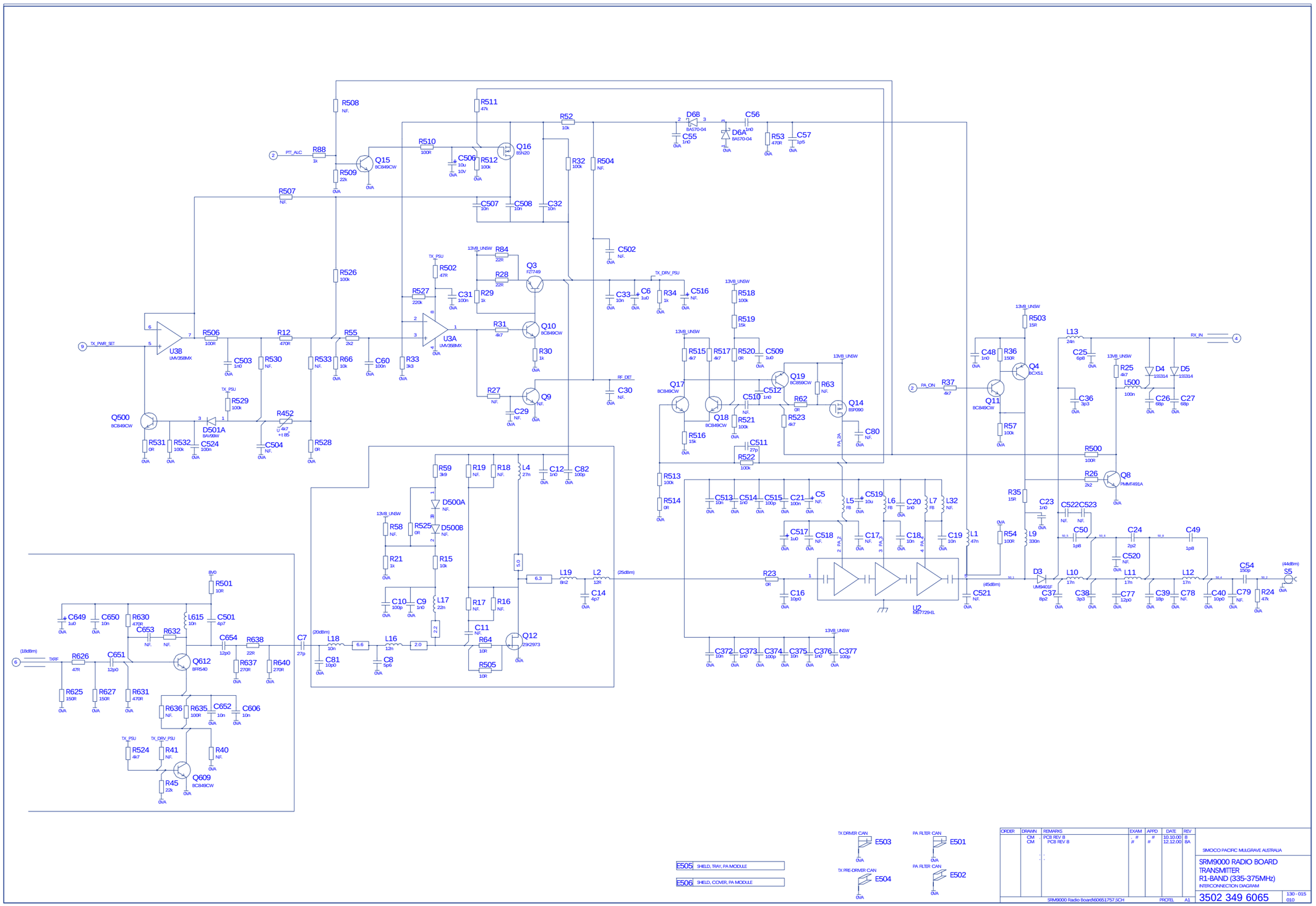


Figure 28 Transmitter (R1 Band) Schematic

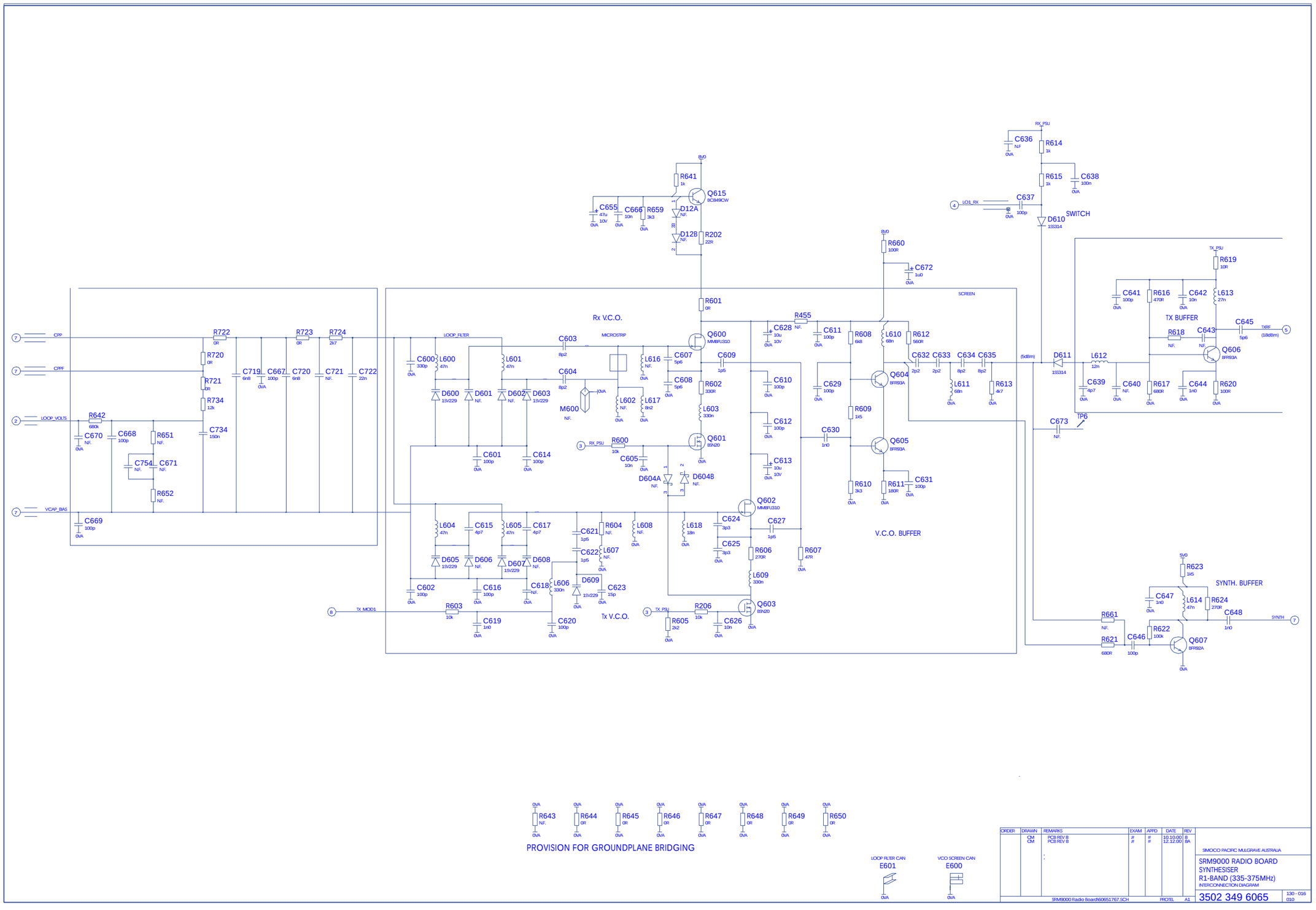


Figure 29 Synthesiser VCO (R1 Band) Schematic

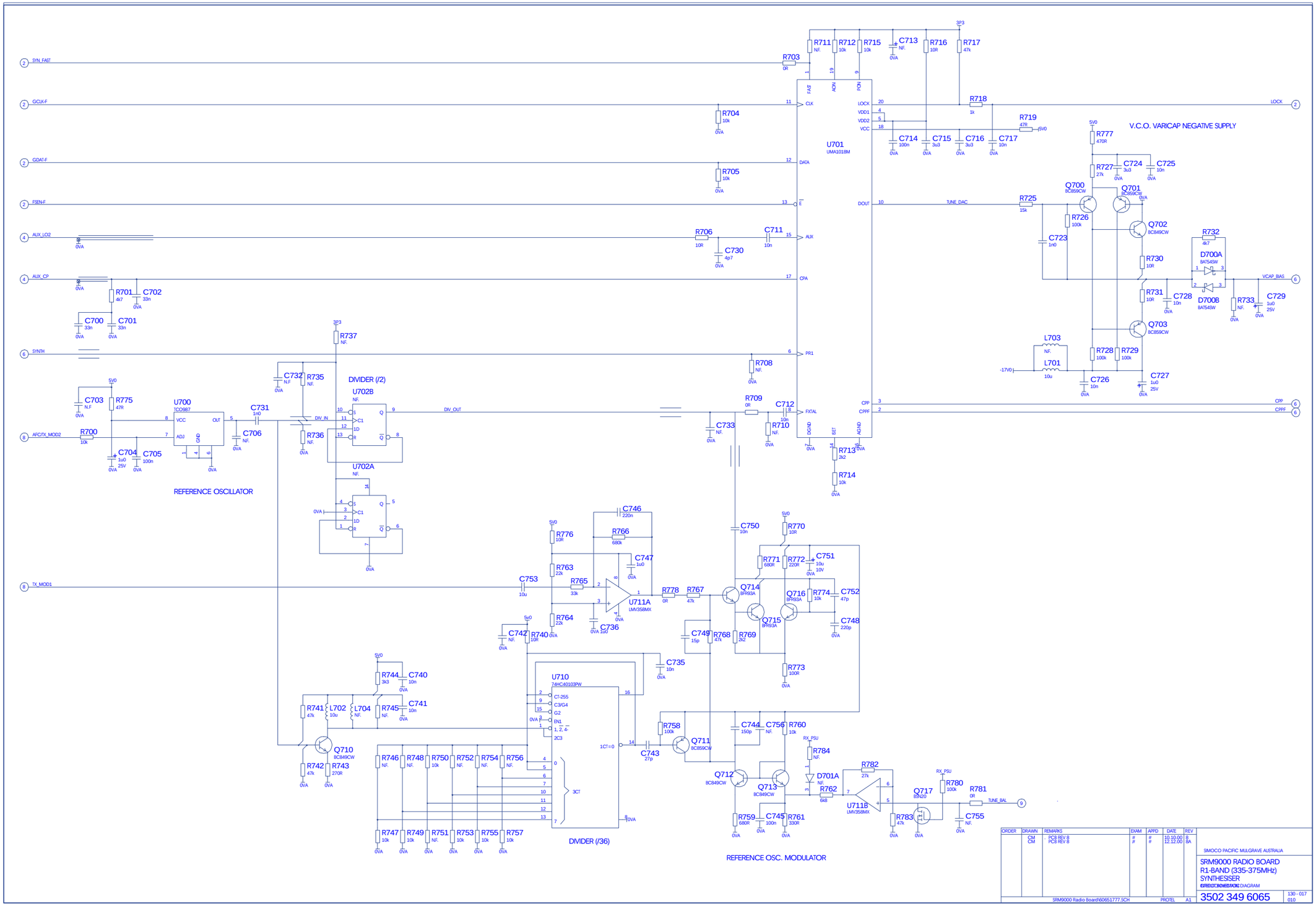


Figure 30 Synthesiser PLL (R1 Band) Schematic

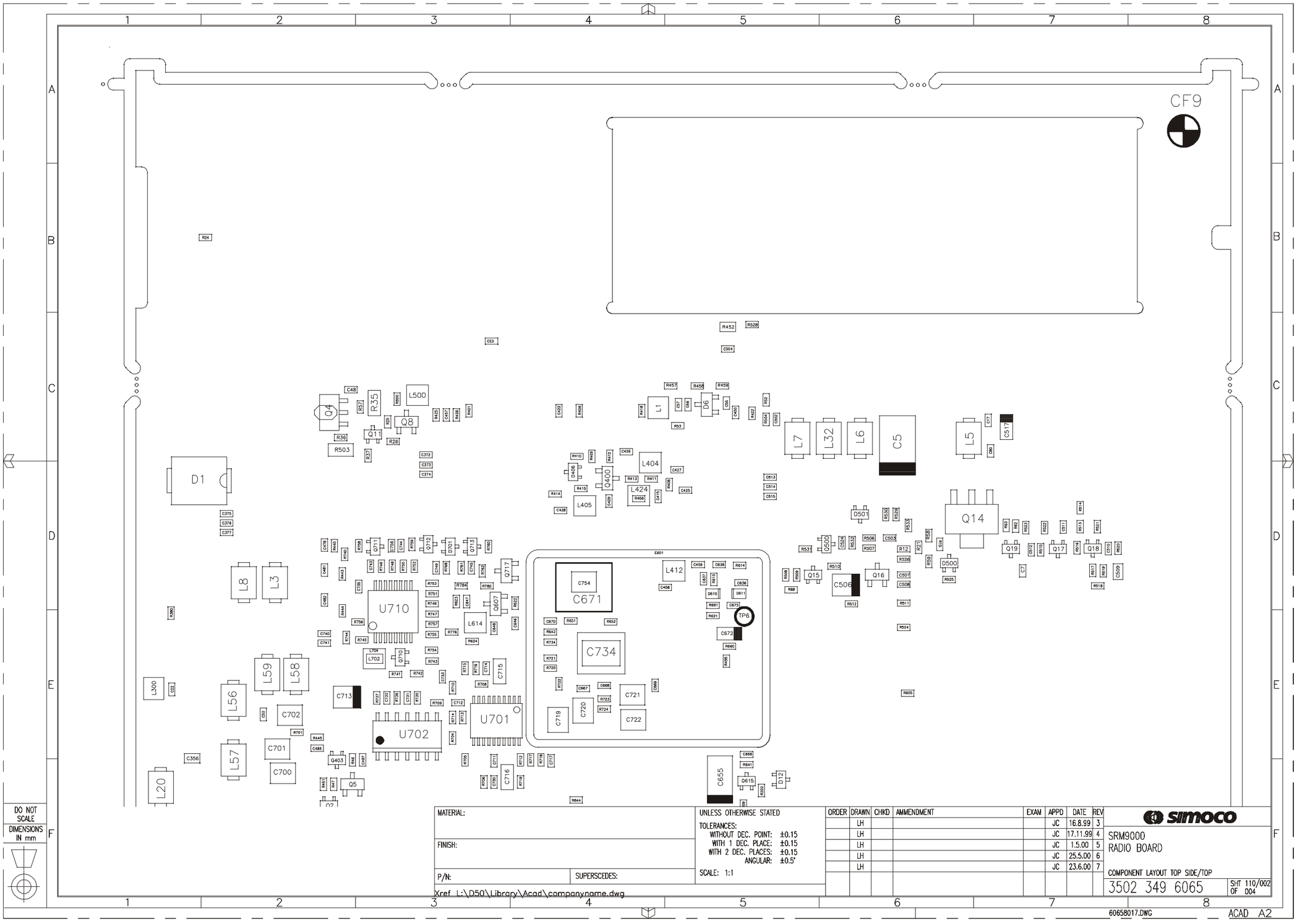


Figure 31 PCB Layout - Top side / Top

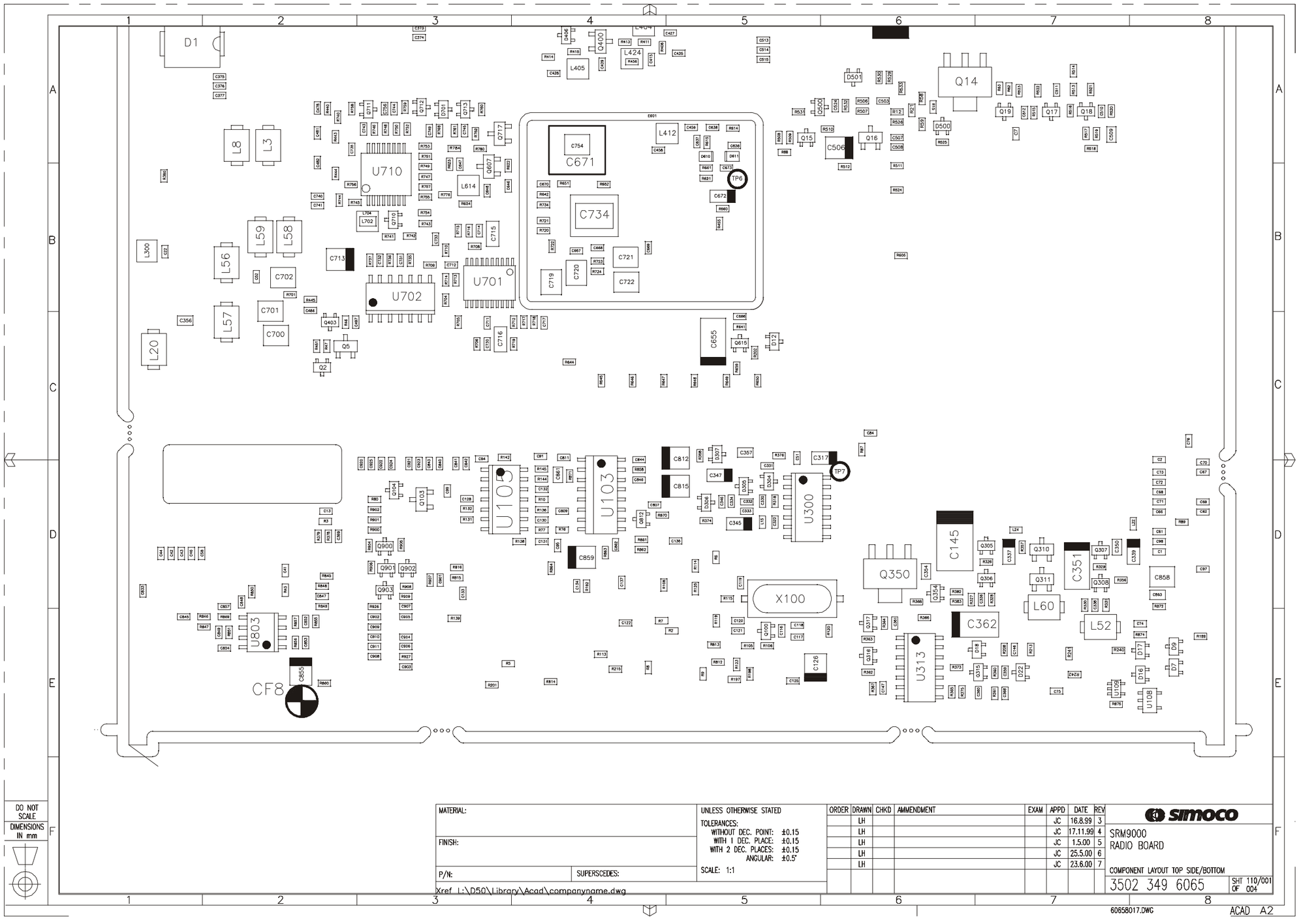


Figure 32 PCB Layout - Top side / Bottom

Figure 33 PCB Layout - Bottom side / Top

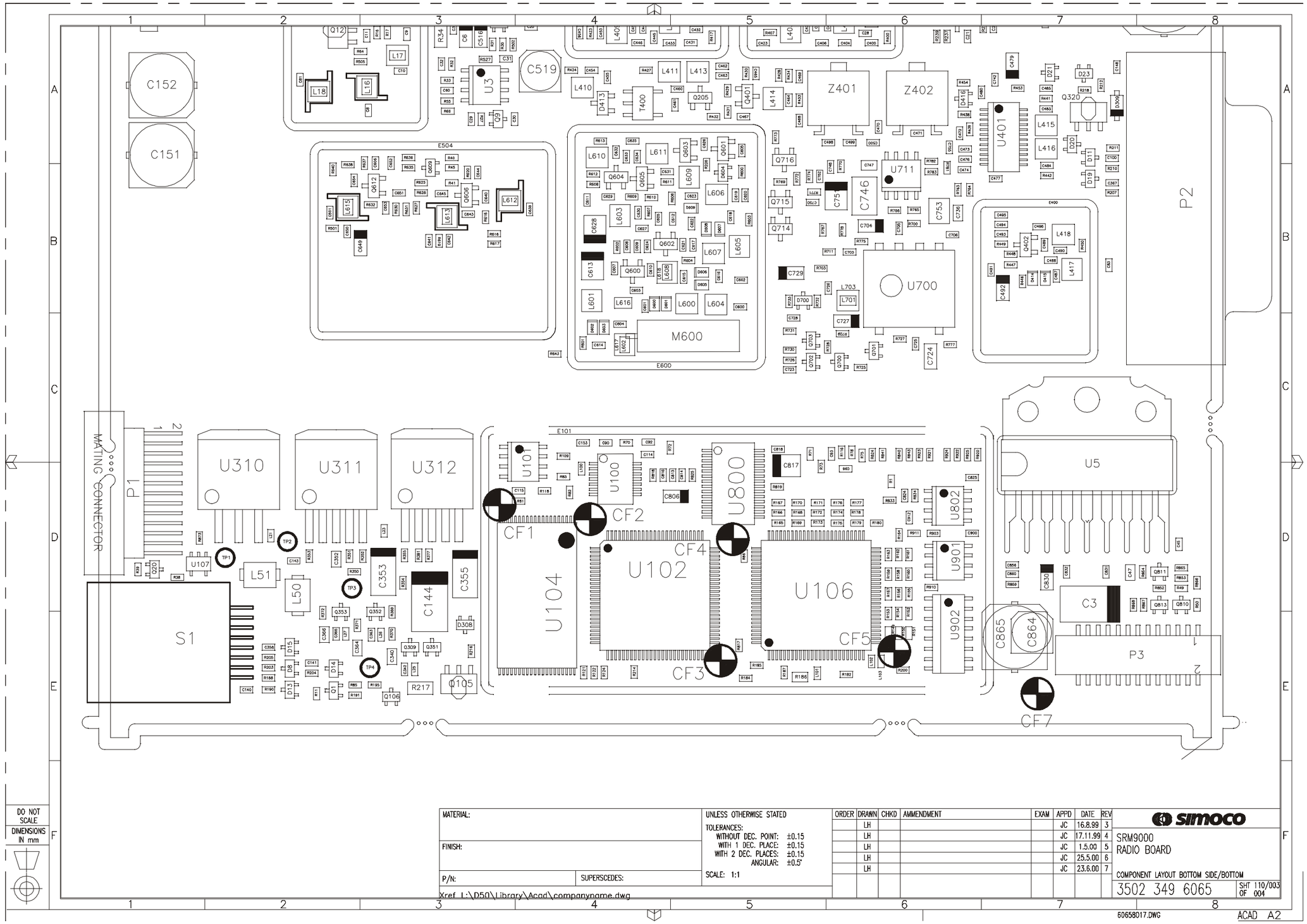


Figure 34 PCB Layout - Bottom side / Bottom